

ANDROBOT, INC.

TITLE

TOPO BATTERY MONITOR

SIZE	CODE	NUMBER	REV
B		11-010175-001	A

DATE 10/18/33 SHEET 2 OF 3

8

7

6

5

4

3

2

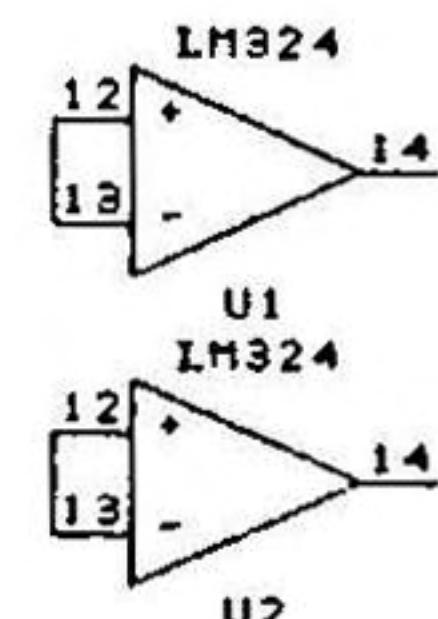
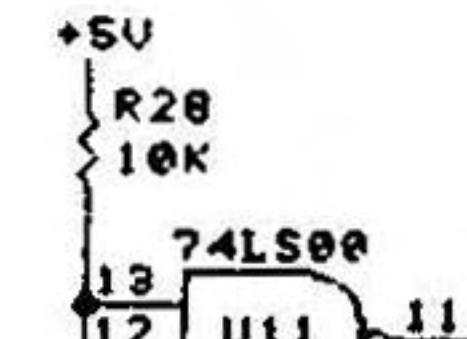
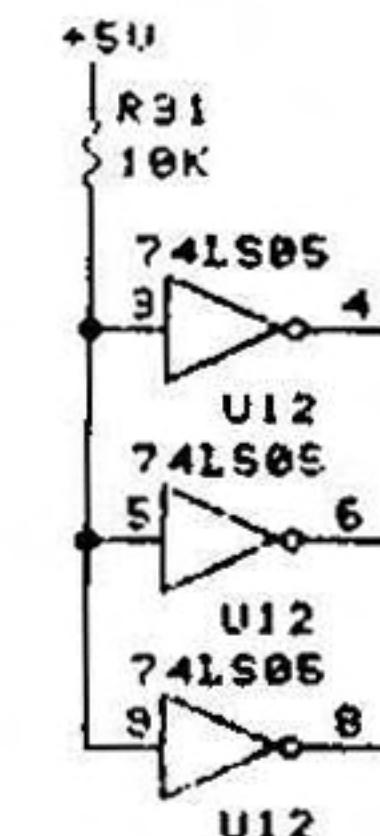
1

REVISONS

ZONE	LTC	DESCRIPTION	DATE	AFUD
	A	Rev 1.00 Rev A	1/10/83	LH
	B	Review Electricals	1/15/83	X

IC TABLE

IC TYPE	REF DESIGNATOR	QTY	+5V	8ND	-5V	+12	+24
8031	U8	1	48	28			
2764	U9	1	26	14			
6116	U7	1	24	12			
4013	U6	1	14	87			
4071	U4, U18	2	14	87			
4584	U3	1	14	87			
74LS08	U11	1	14	87			
74LS05	U12	1	14	87			
74LS373	U5	1	26	16			
LM324	U1, U2	2	64	11			
LM7605	UR1	1	63	82	81		
S63635	Q1, Q2, Q3, Q4	4					81

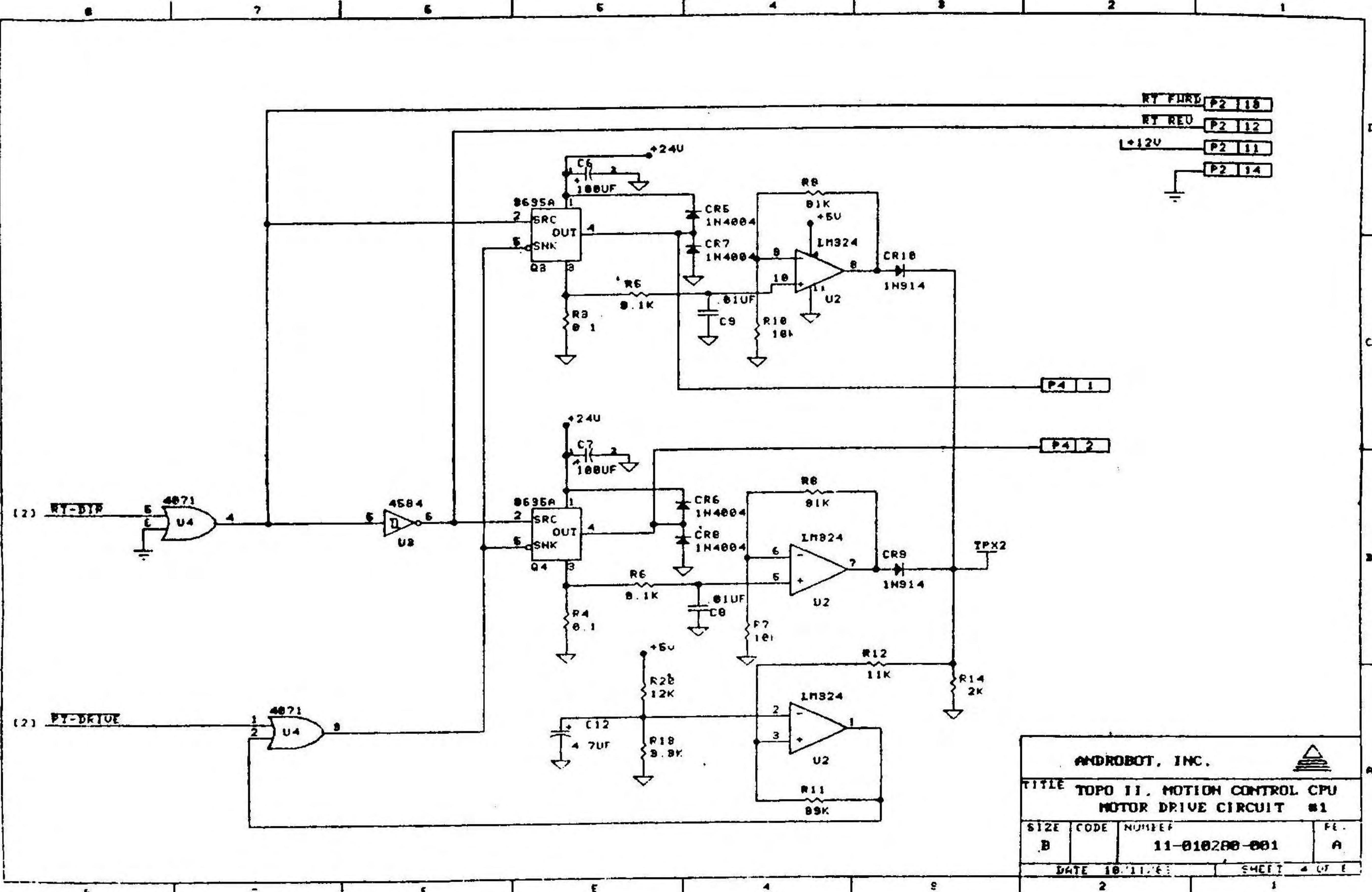


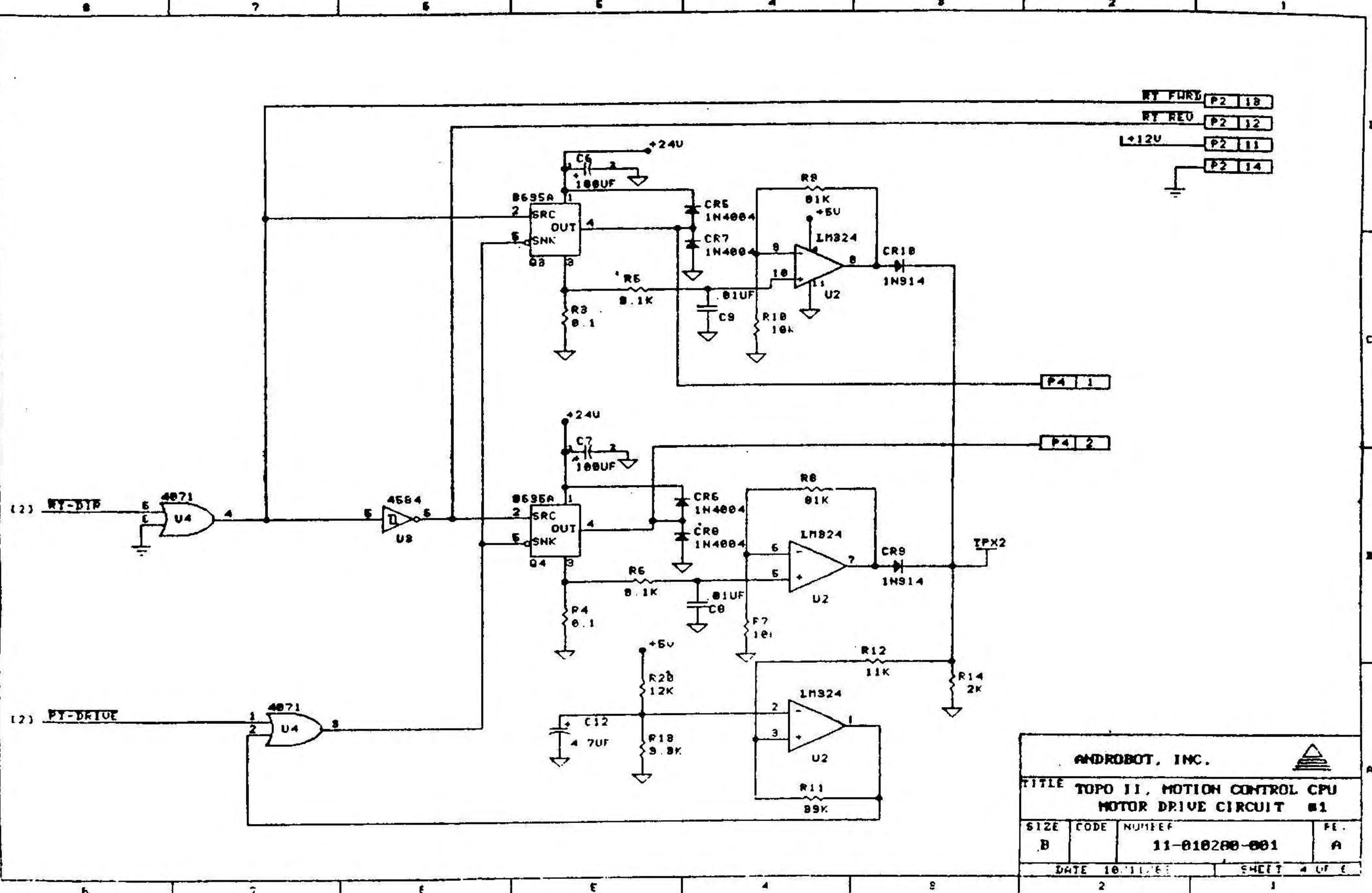
MAR 28 1984

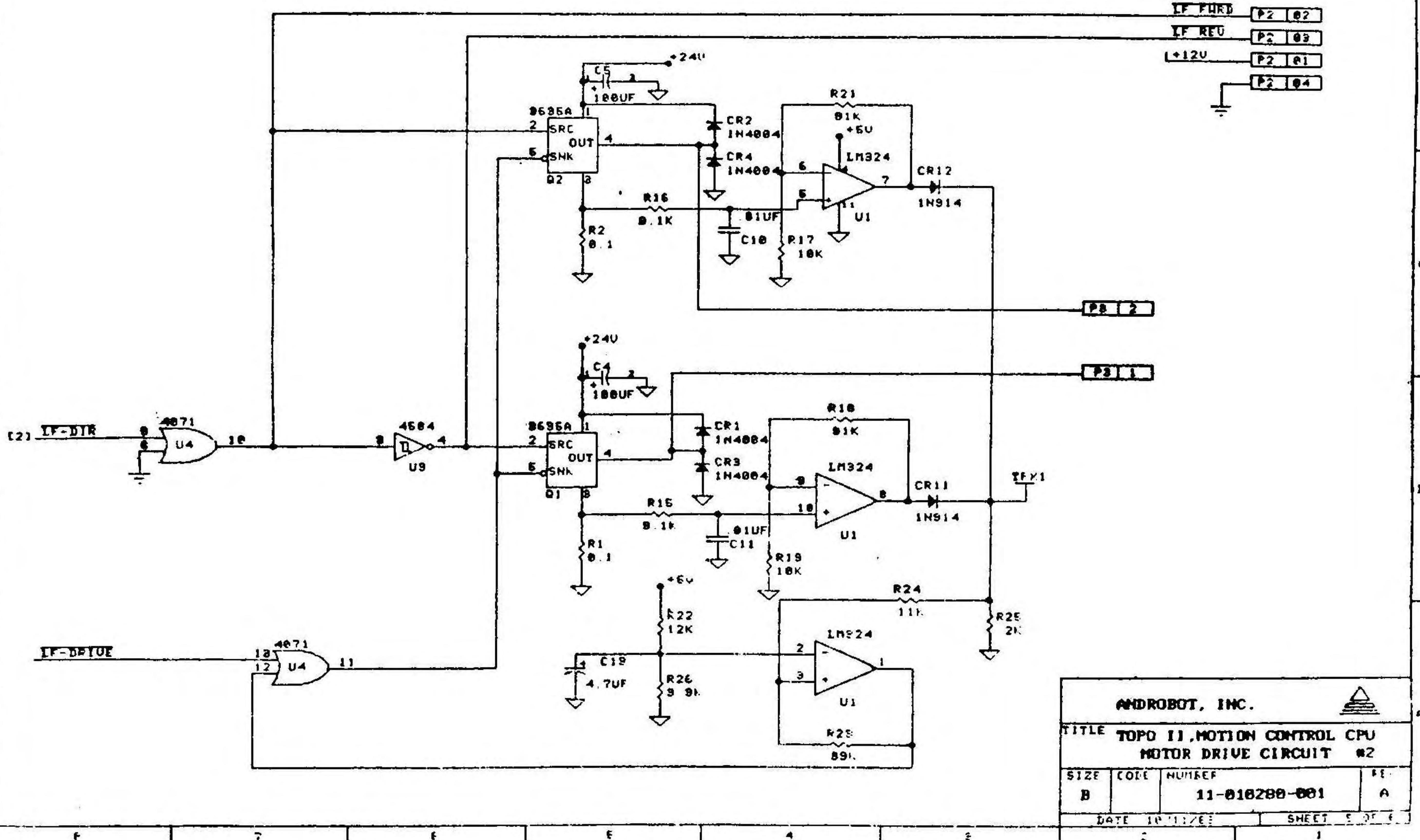
NOTE

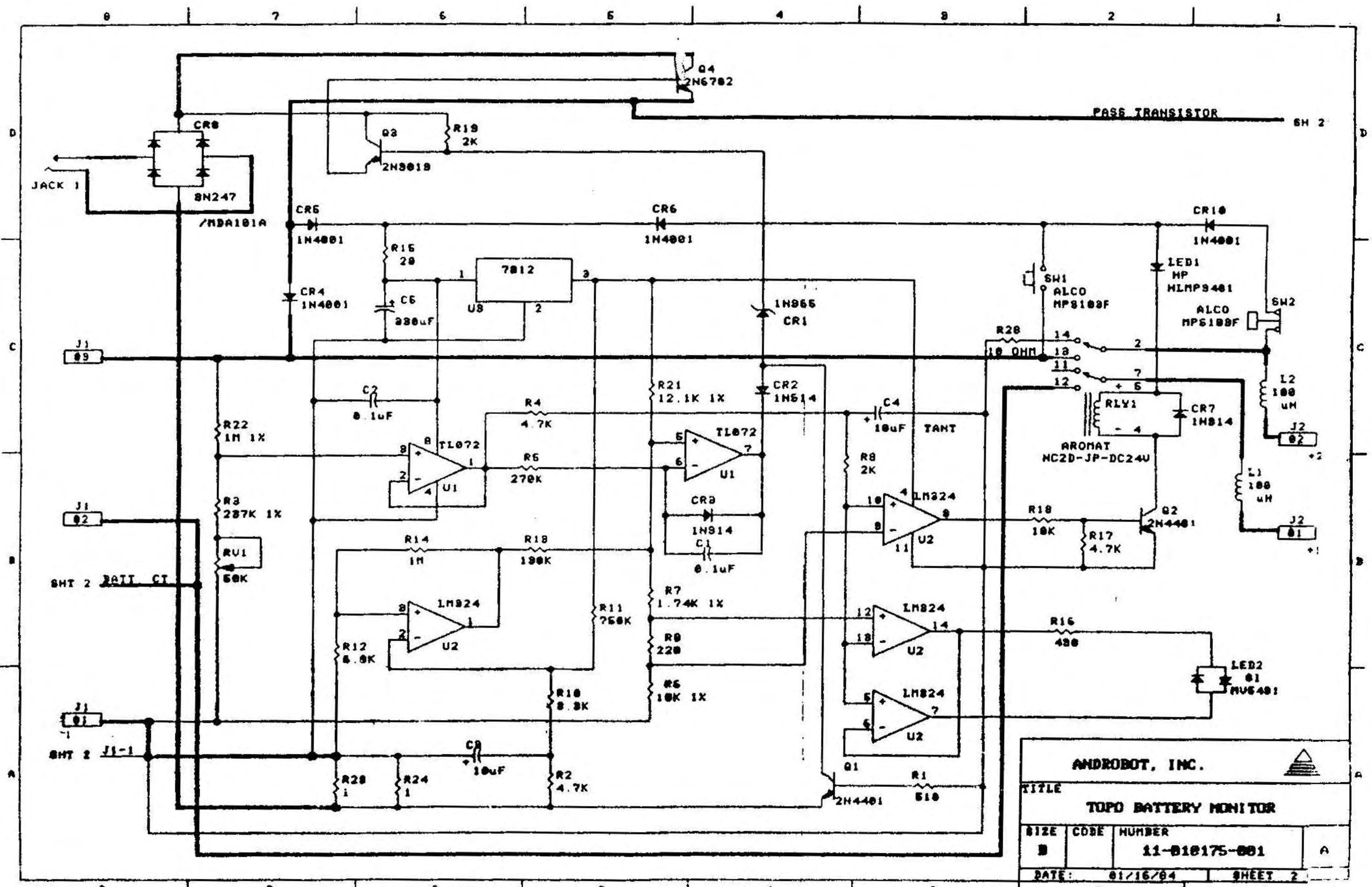
- 1 ALL BYPASS CAPACITORS ARE .1UF IN VALUE.
- 2 DESIGNATORS FOR CAPS ARE C14 THRO C23

DATE	4-28-84	BY	JY
AFUD		AFUD	
ENGRB		TITLE	TOPO II, MOTION CONTROL CPU COVER SHEET
CHKR		SIZE	B
		CODE	
		NUMBER	11-010200-001
		REV	B
		SHEET 1 OF 6	



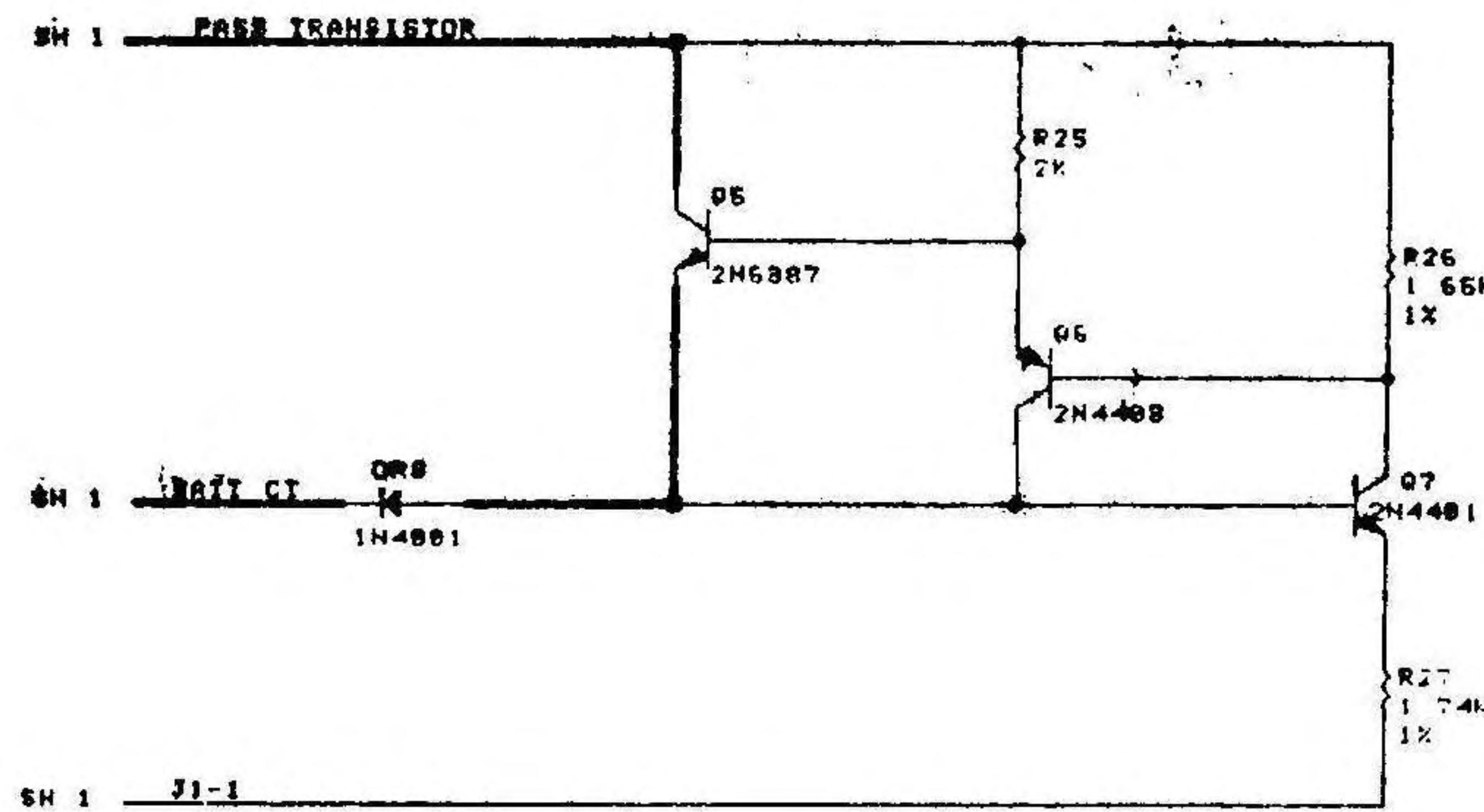






PRE PRODUCTION RELEASE

MAR 28 1984

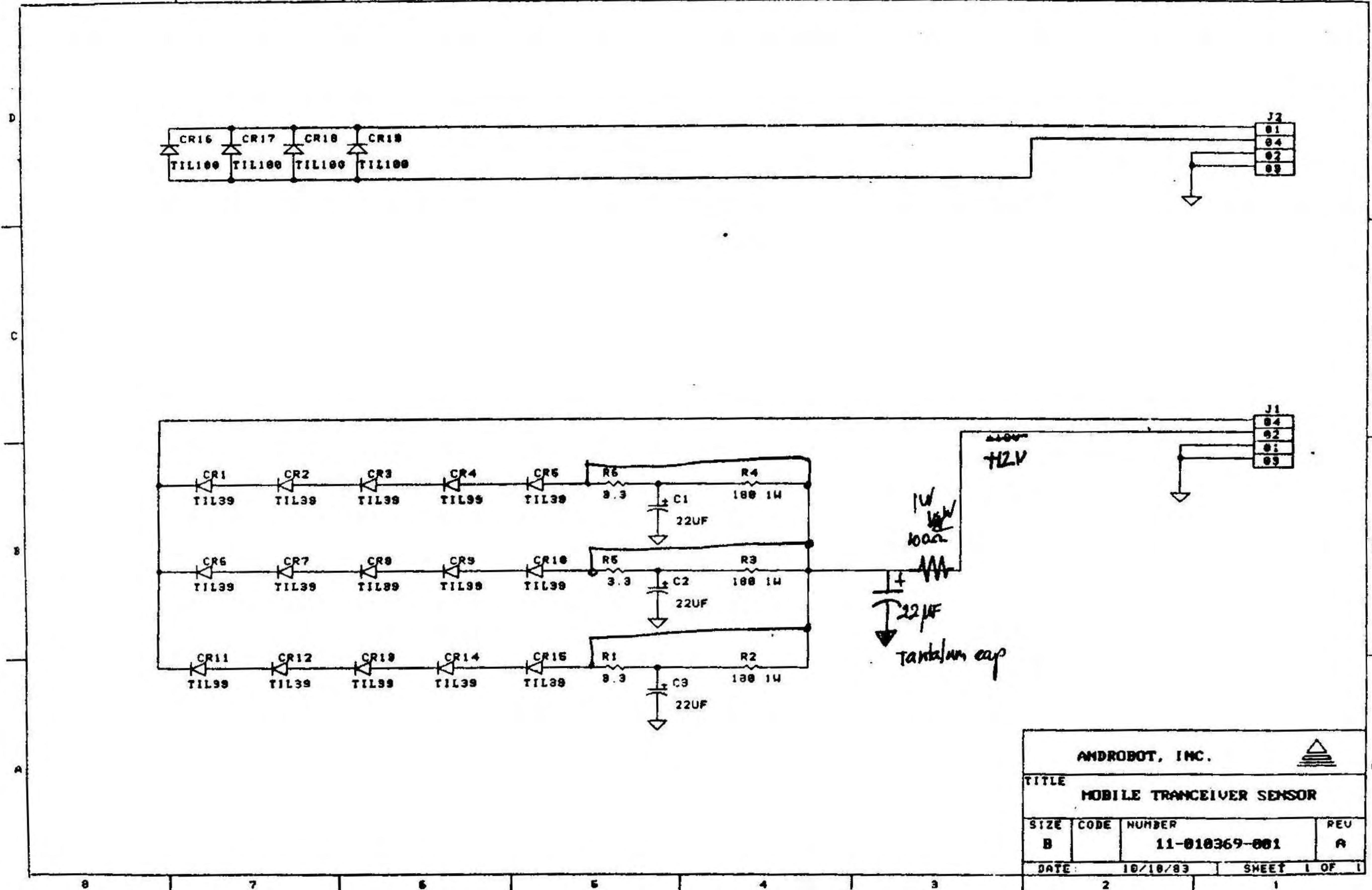


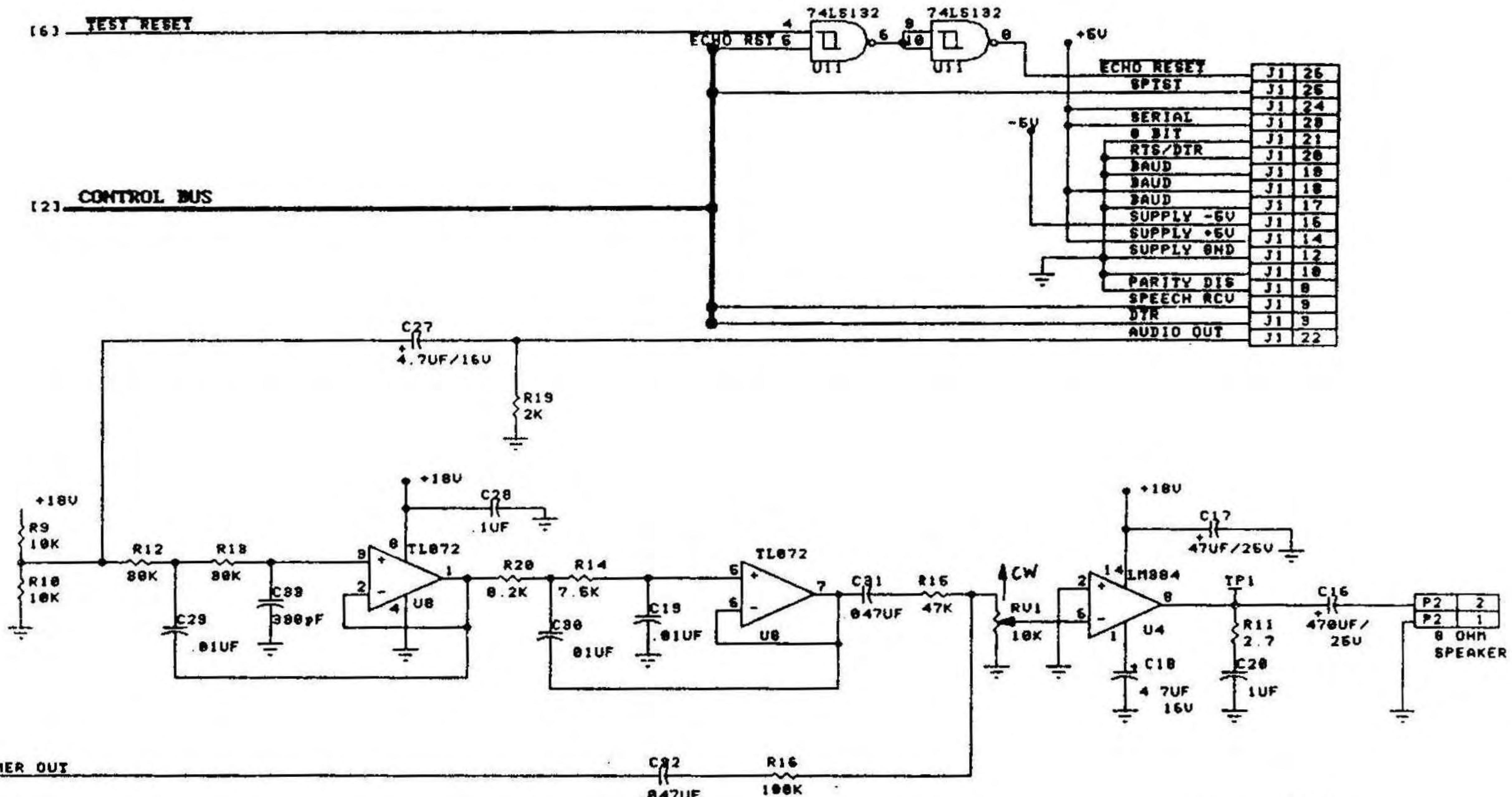
DATE 12/05/88
DMM J. YOUNG
APU
APUD
ENRCC
CMC

ANDROBOT, INC.

TOPO BATTERY MONITOR

8 7 6 5 4 3 2 1





(8) TIMER OUT

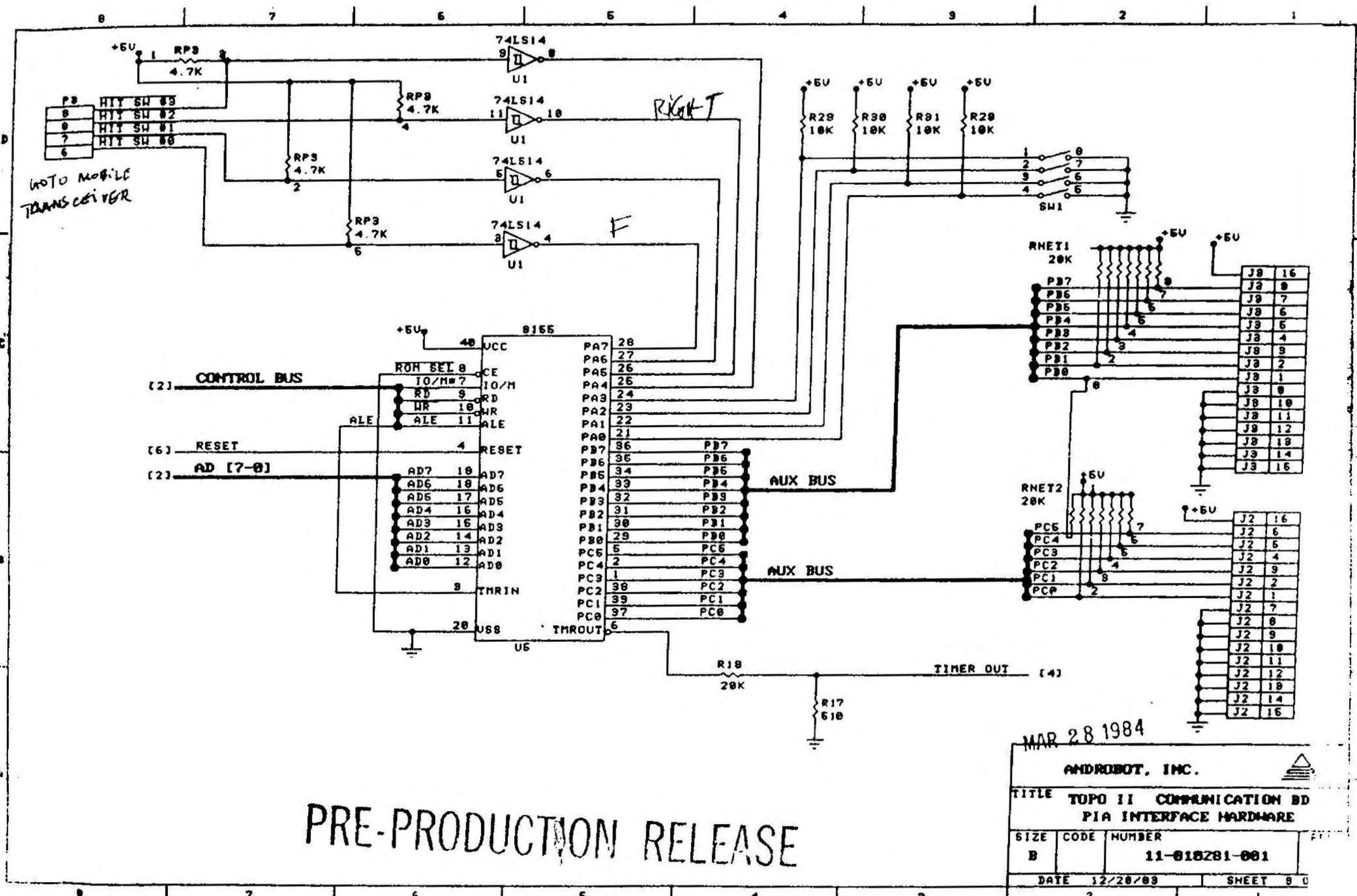
PRE-PRODUCTION REVISION

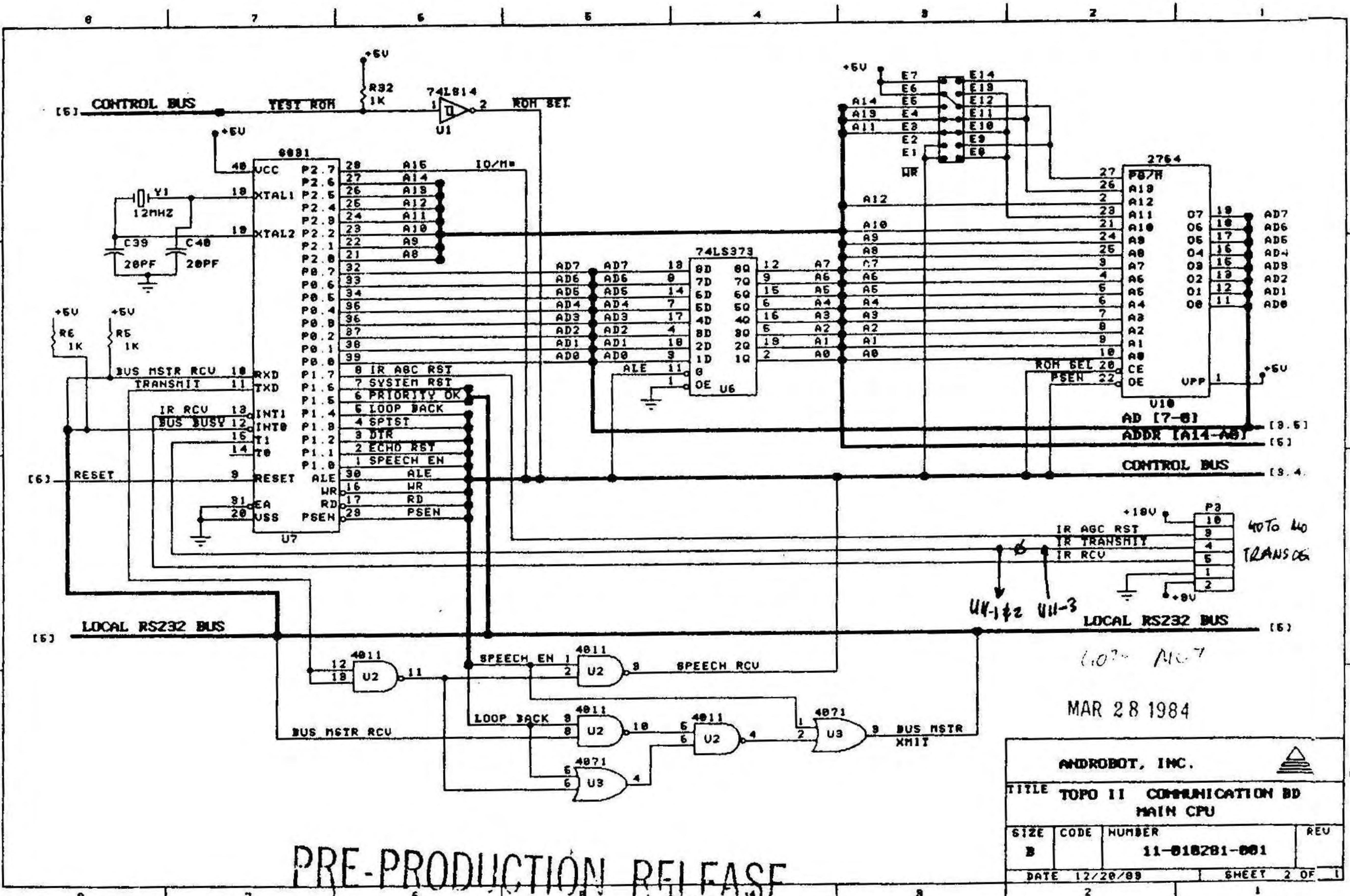
MAR 28 1984

ANDROBOT, INC.

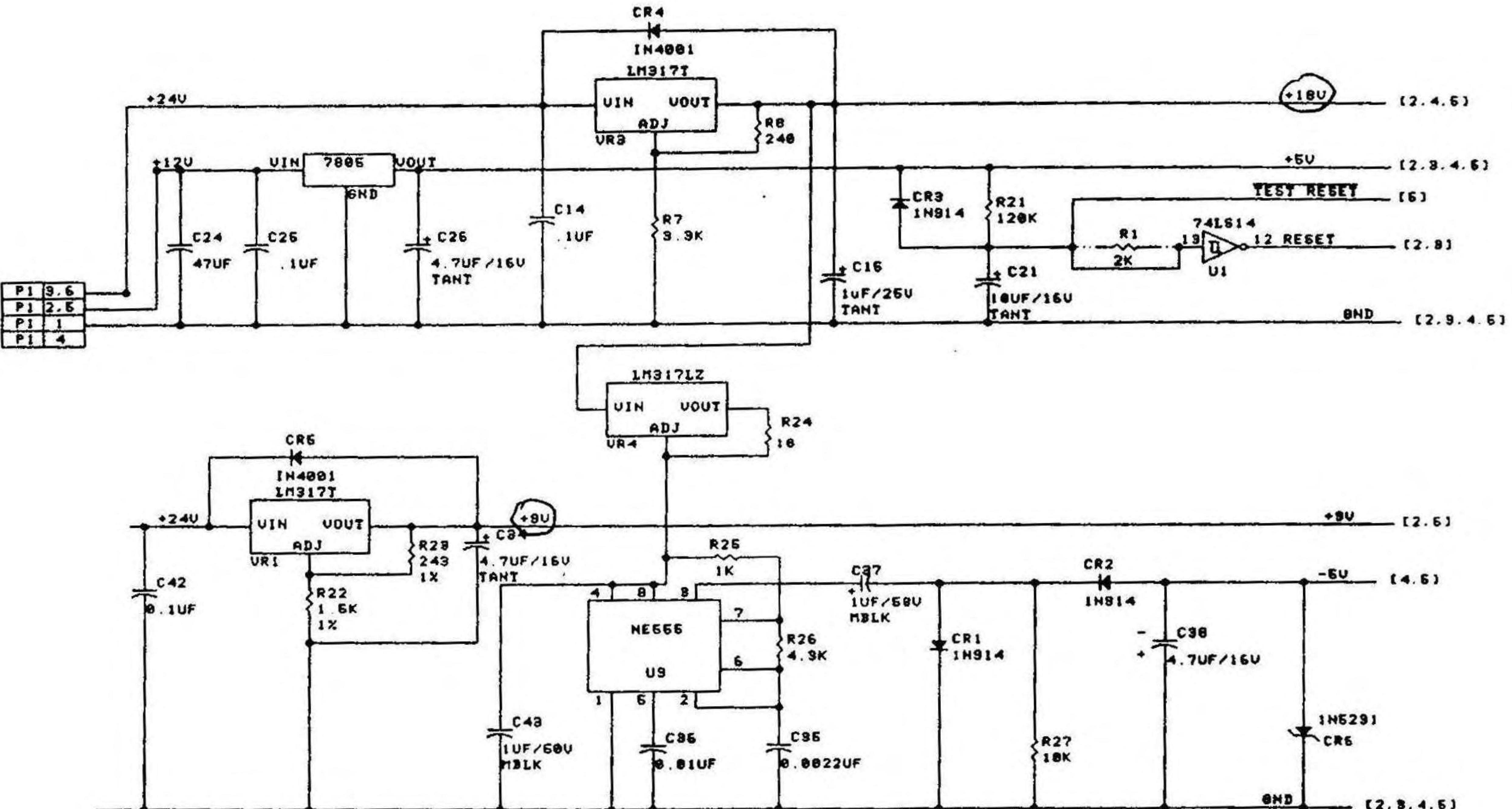
TITLE TOPO II COMMUNICATION BD
ECHO SPEECH MODULE INTERFACE

SIZE	CODE	NUMBER	REV
B		11-010281-001	R
DATE	12/20/83	SHEET 4 OF 1	





8 7 6 5 4 3 2 1

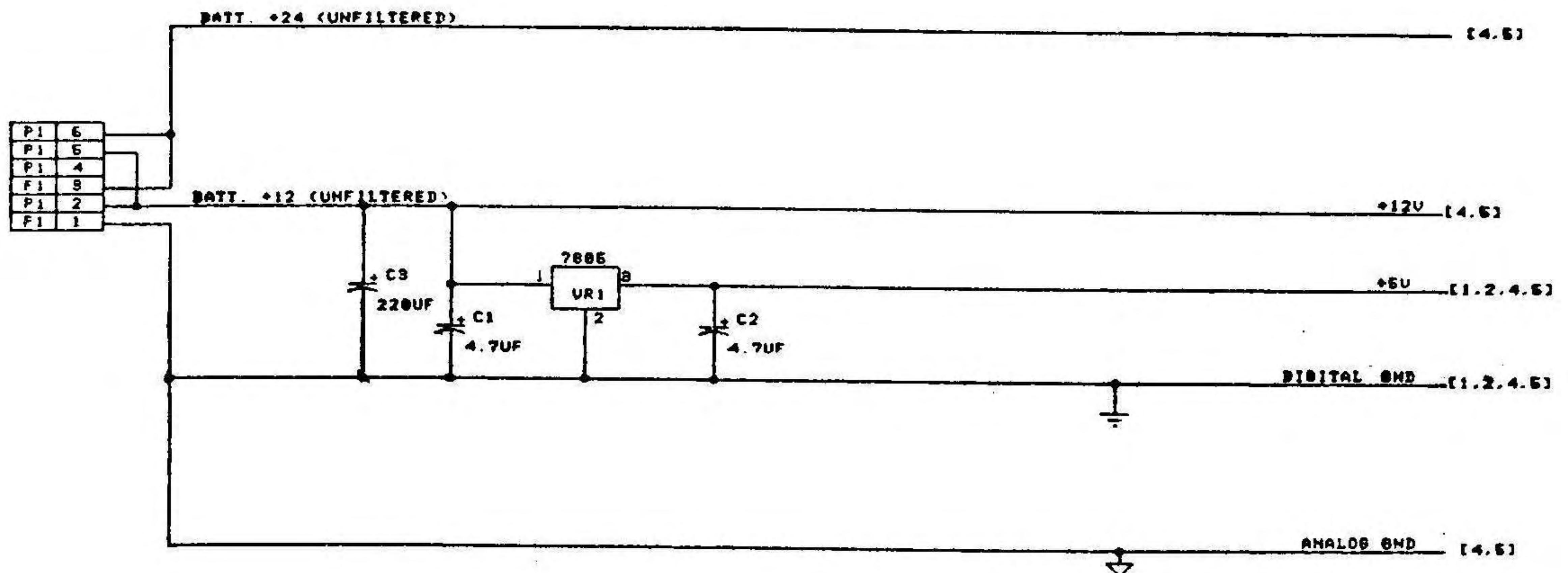
**NOTES**

- IF R1 IS INSTALLED THEN THE TRACE ON CIRCUIT SIDE OF BOARD MUST BE CUT BETWEEN SOLDER PADS OF R1 (SEE NOTE ON ASSEMBLY DWG. FOR EXACT PLACE OF CUT)

MAR 28 1984

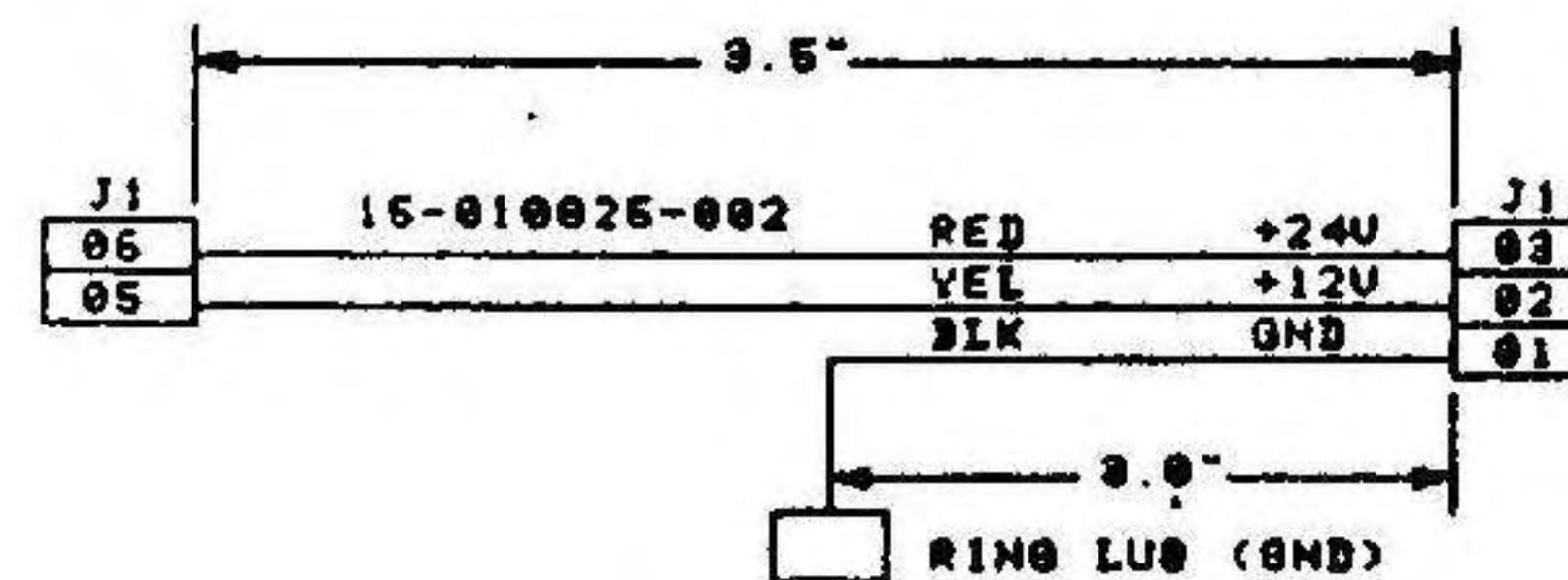
ANDROBOT, INC.

TITLE			TOPO II COMMUNICATION BI
			POWER SUPPLY & RESET
SIZE	CODE	NUMBER	REU
B		11-810281-001	
DATE	12/28/83		SHEET 6 OF 1



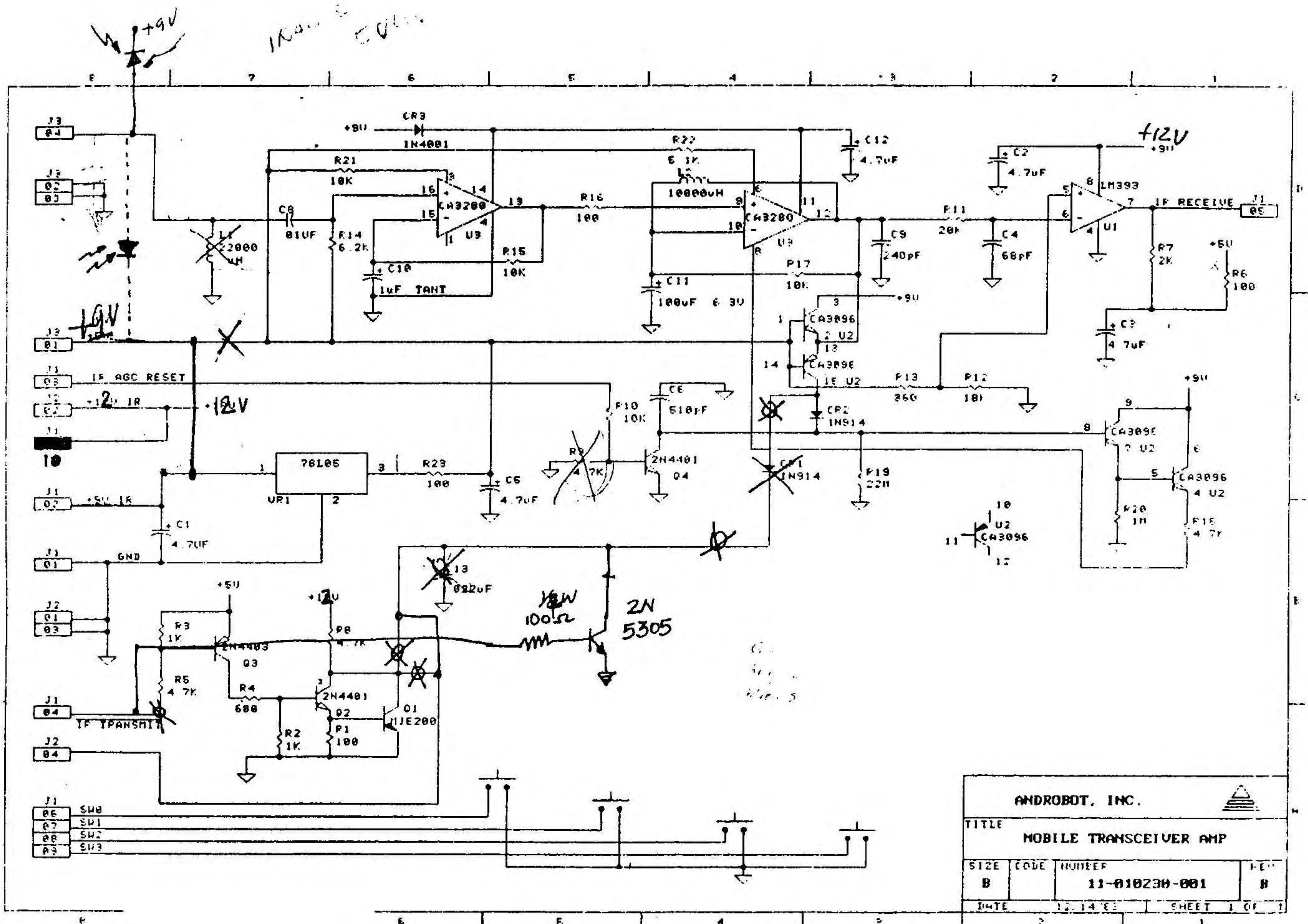
ANDROBOT, INC.			
TITLE TOPO II, MOTION CONTROL CPU POWER SUPPLY			
SIZE	C/C	NUMBER	REV
B		11-010200-001	A
DATE	10/1/83	SHEET	1 OF 1

REVISED				
ZONE	LTR	DESCRIPTION	DATE	APUD

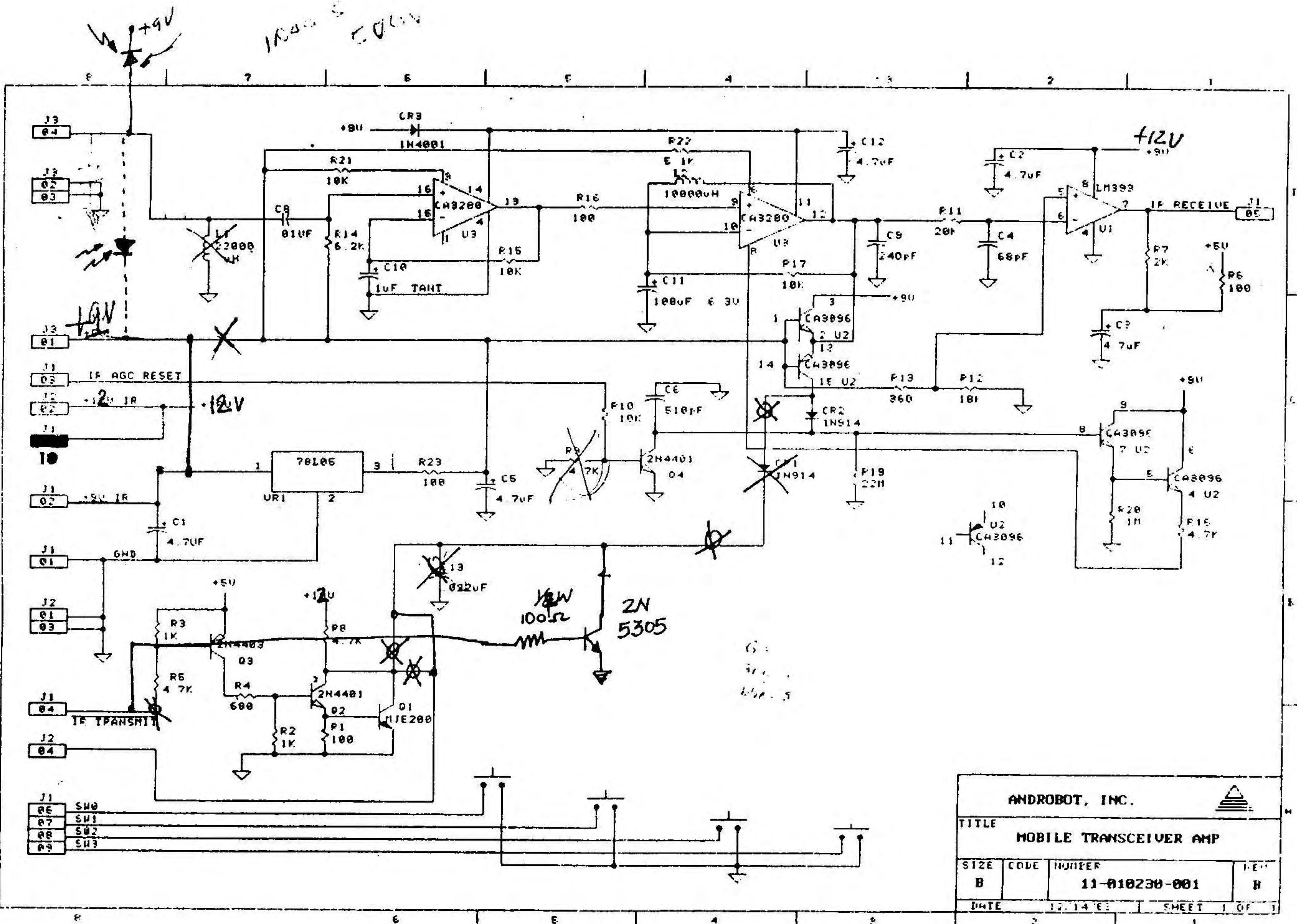


DATE	10/20/83	ANDROBOT, INC.		
DWN	T. SAKAMOTO			
APUD				
APUD				
ENGRG		TITLE		
CHKR		TOPO II		
		POWER DISTRIBUTION HARNESS ASS'Y		
SIZE	CODE	NUMBER	REV	
B		15-010025-002	A	

SHEET 2 OF 2



ANDROBOT, INC.		
TITLE MOBILE TRANSCEIVER AMP		
SIZE B	CODE 11-010230-001	NUMBER B
DATE 12.14.83	2	SHEET 1 OF 1



ANDROBOT, INC.

TITLE

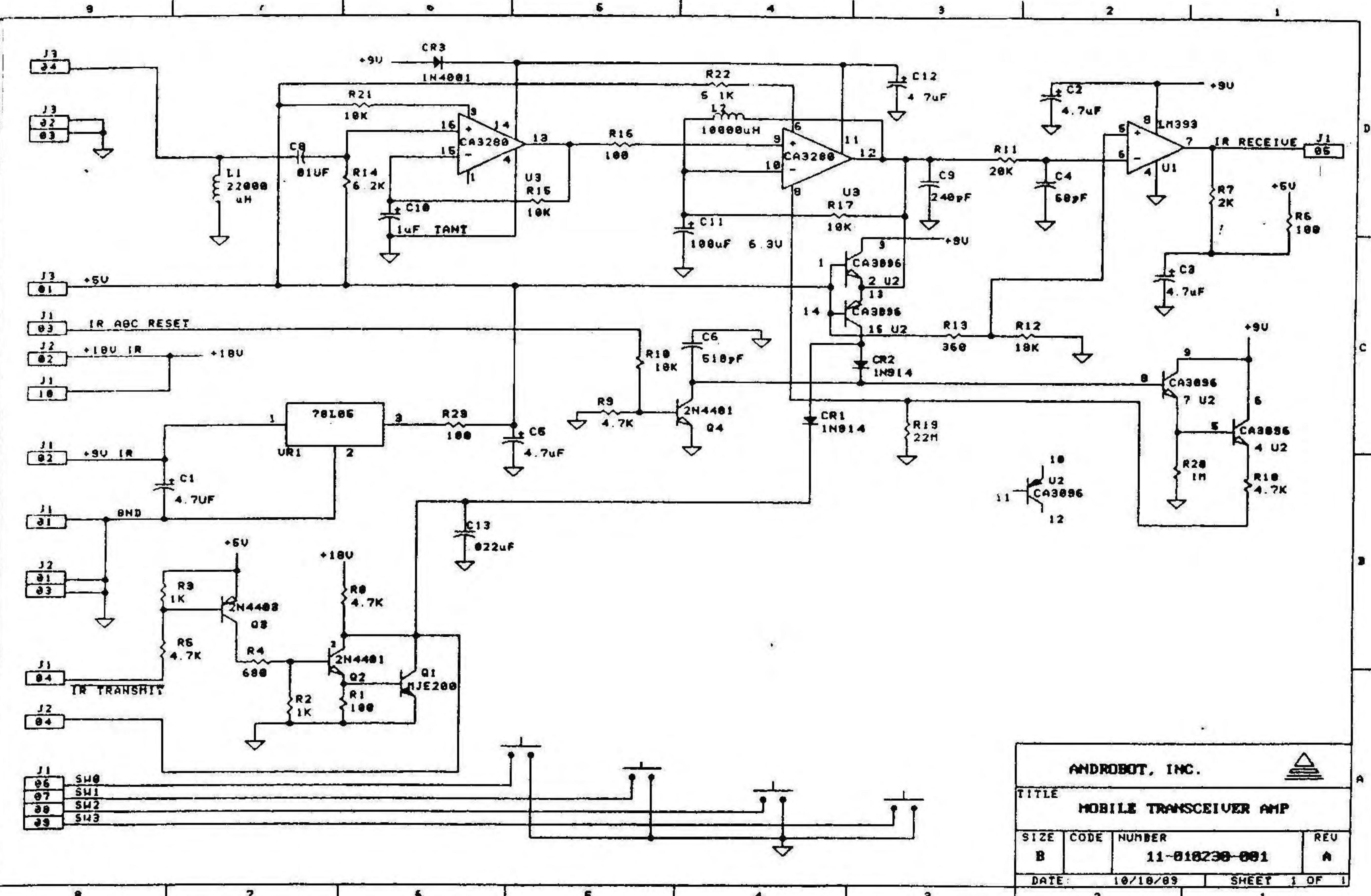
MOBILE TRANSCEIVER AMP

S12

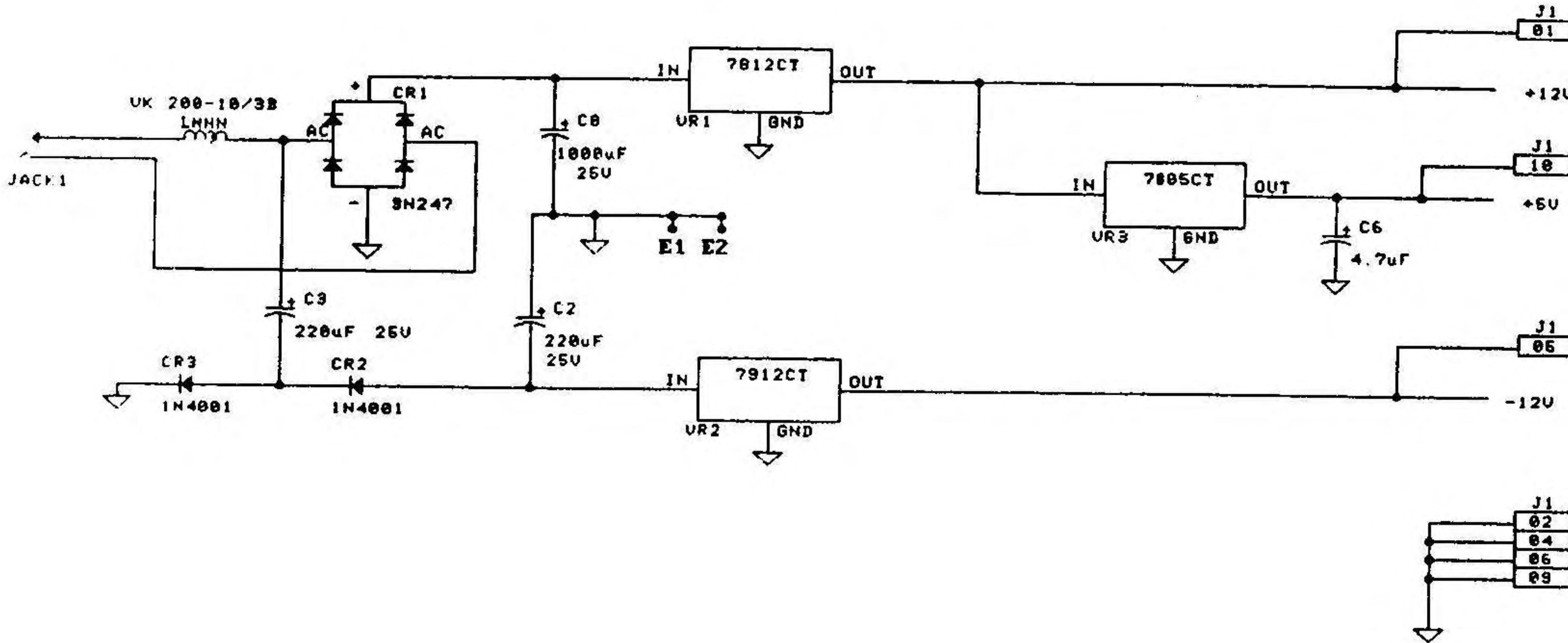
11-01023H-001

1

DATE 12-14-03 SHEET 1 OF 1



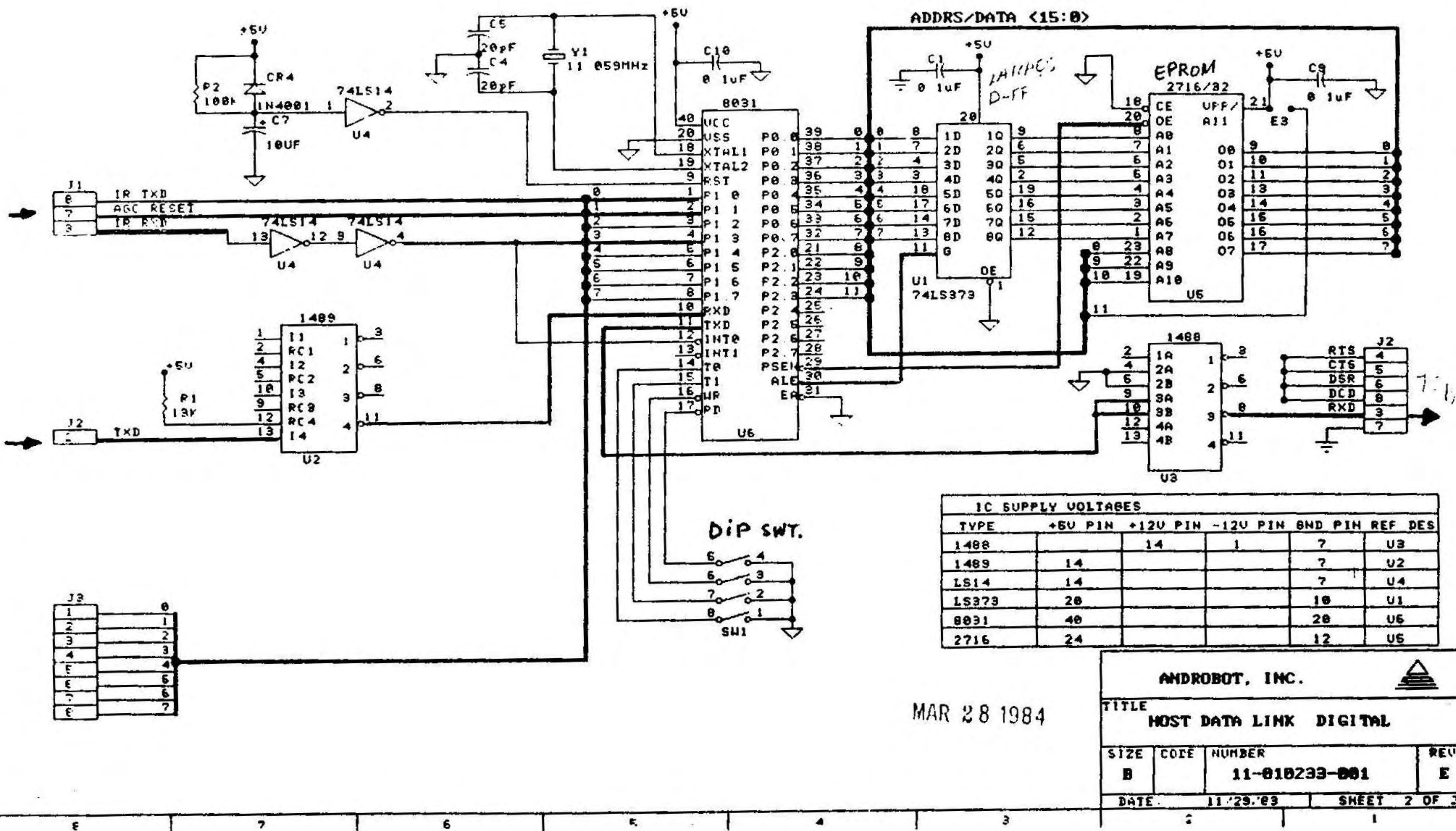
REVISTORS			
LTR	DESCRIPTION	DATE	APPUL
H	PRE-PROD REL. ECO 00020	10-26-83	LH
B	REUISE PER ECO 00030. SHT 2. ZONE D12	11-02-83	LH
C	RELLOCATE 9 PIN CONN . ECO 00036	11-09-83	LH
D	PEU SHT 7. ECO 00054	11-30-83	J. YOUNG
E	REVISED PER ECO 00092	11-30-83	DR

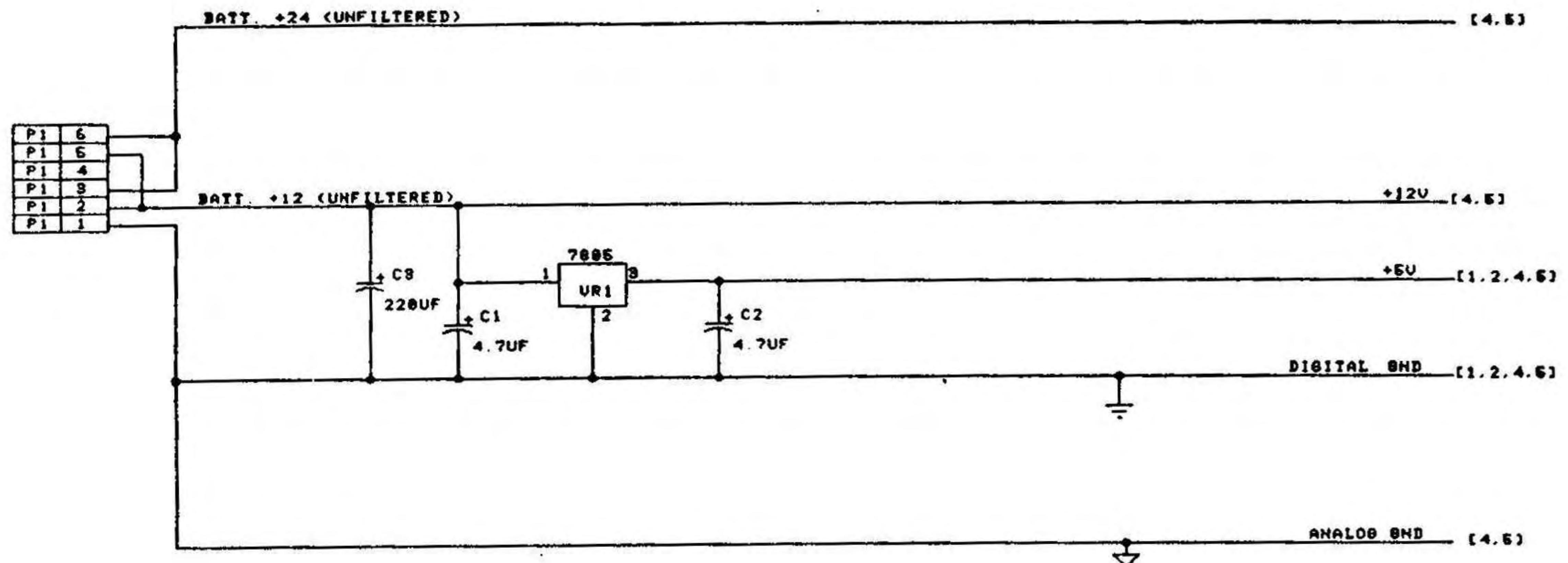


PRE-PRODUCTION RELEASE

DATE : 11-09-83	ANDROBOT, INC.			
DWN : J.K				
CHK :	TITLE			
J. YOUNG	HOST DATA LINK DIGITAL			
APPUL:	SIZE	CODE	NUMBER	REV
DOC : L. MAIDOR	B		11-010233-001	E
CONT :	DATE	11/29/83		SHEET 1 OF 2

PRE-PRODUCTION RELEASE

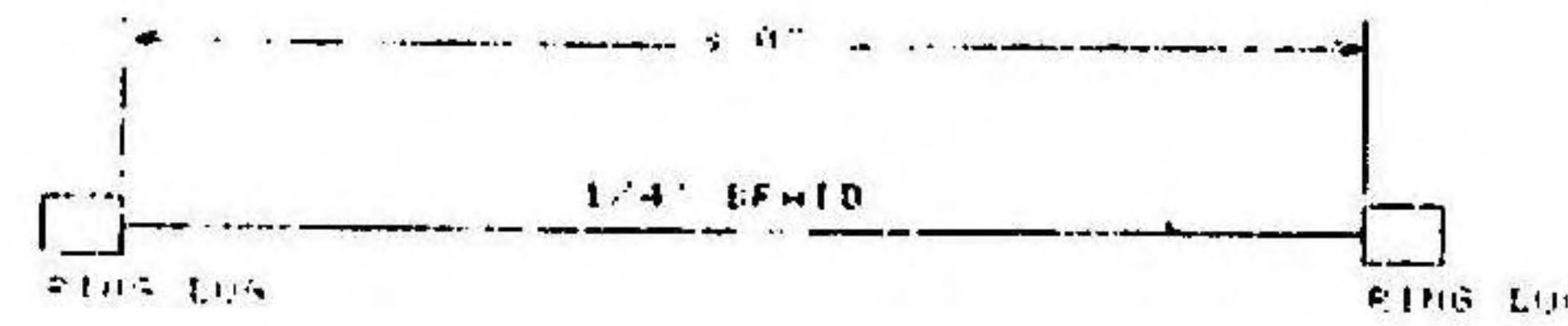




ANDROBOT, INC.			▲
TITLE TOPO II, MOTION CONTROL CPU POWER SUPPLY			
SIZE B	CODE 11-010288-001	NUMBER	REV B
DATE 11/02/83	2	SHEET 3 OF 6	

MAR 28 1984

PRE-PRODUCTION RELEASE



DATE	10/13/07	REVISION	0
DESIGNER	T. COOPER	APPROV.	
APPROV.		CHIEF	
CHIEF		SIZE	CODE
		B	
			NUMBER
			15-810829-081
			REV
			A

ANDROBOT, INC.

TITLE: TOPO II
CAGE/CHASSIS GROUND STRAP

SHEET 1 OF 1

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
2			



DATE	10/19/89
DWN	T. SAKAMOTO
APUD	
APUD	
ENGR6	7/7
CHKR	

ANDROBOT, INC.

TITLE
TOPO II
BATTERY GROUND STRAP

SIZE	CODE	NUMBER	REV
B		15-01830-001	A

SHEET 1 OF 1

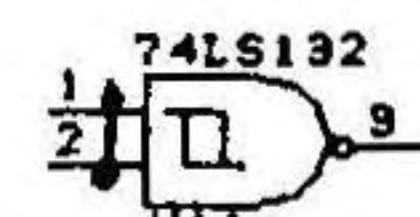
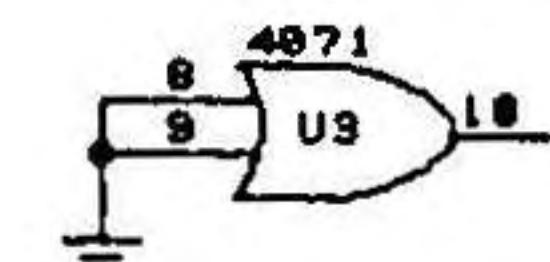
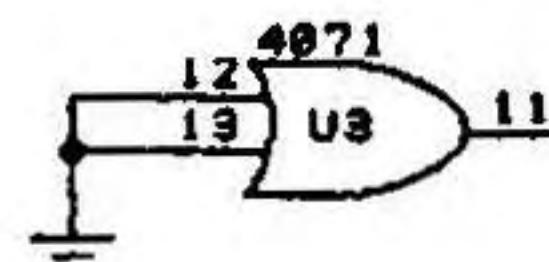
8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

REVISIONS

REV	DESCRIPTION	DATE	APUD
A	PRE-PROD ECO 00019	10/21/83	1.H.
B	REVISED ECO 00032	11/08/83	1.H.
C	REVISED ECO 00068	12/09/83	1.H.
D	REVISED ECO 00072	12/16/83	1.H.
E	REVISED ECO 00078	12/20/83	

IC TABLE

IC TYPE	REF. DES.	+5V	+10	+5V	SHD	+24	+12
4011	U2			14	87		
4071	U8			14	87		
74LS132	U11			14	87		
74LS14	U1			14	87		
74LS878	U6			20	10		
2764	U10			20	14		
6031-A	U7			40	20		
8156	U5			40	20		
TI072	UB			68	64		
LM384	U4			14	7.9		
					10.4		
656	UB					81	
LM317T	UR1	VOUT				VIN	
LM317T	UR3	VOUT				VIN	
LM7806	UR2			68	62		81
LM317LZ	UR4	VIN					



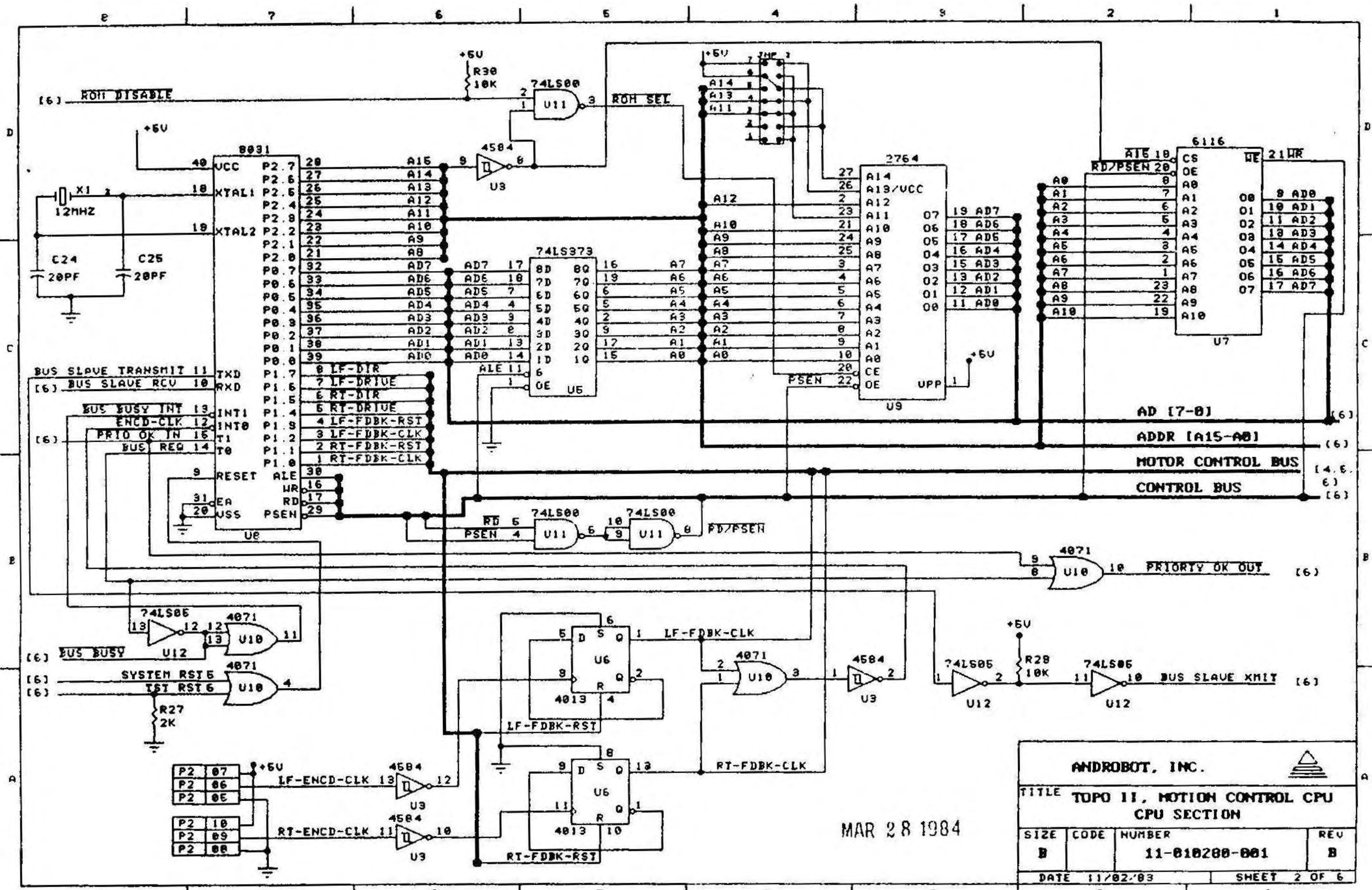
NOTES

1. 0.1UF BYPASS CAPS ARE C11-C18,C22,C29,C41,C44.
2. OPTIONAL PARTS FOR U1 ARE: 74HC14 OR MC14584
HOWEVER IF A 14584 IS USED THE AUTO-TESTER
CANNOT BE USED TO TEST ALL FUNCTIONS OF THE
BOARD.
3. IF EITHER OPTIONAL PART IS USED IN U1 THEN R1
MUST BE INSTALLED. [SEE SHT. #6]

PRE-PRODUCTION RELEASE

MAR 28 1984

DATE	12/20/83	ANDROBOT, INC.		
DWN	J. YOUNG			
APUD				
APUD				
ENRG				
CHKR				
TITLE TOPO II COMMUNICATION BD				
MAIN CPU UNUSED GATES, IC LIST				
SIZE	CODE	NUMBER	REL	
B		11-010281-001	E	
SHEET 1 OF 1				



PRF - PRODUCTION RELEASE

9 ? 6 5 4 1 8 2 1 0

[2] — ADDR [A15-A8]

A16	P2 7	P7 33
A14	P2 6	P7 19
A13	P2 5	P7 34
A12	P2 4	P7 17
A11	P2 3	P7 3E
A10	P2 2	P7 1E
A9	P2 1	P7 8E
A8	P2 0	P7 1E
A7		P7 3?
A6		P7 14
A5		P7 86
A4		P7 18
A3		P7 39
A2		P7 12
A1		P7 48
A0		P7 11
AD7	P8 7	P7 28
AD6	P8 6	P7 22
AD5	P8 5	P7 36
AD4	P8 4	P7 21
AD3	P8 3	P7 91
AD2	P8 2	P7 28
AD1	P8 1	P7 32
AD0	P8 0	P7 18
ALE		P7 9
UF		P7 42
RD		P7 18
PEEN		P7 41

[2] — AD [D7-D0]

[2] — CONTROL BUS

[2] — MOTOR CONTROL BUS

[2] — ROM DISABLE (TEST USE ONLY)
[2] — TEST RESET (TEST USE ONLY)
[8] — *BL

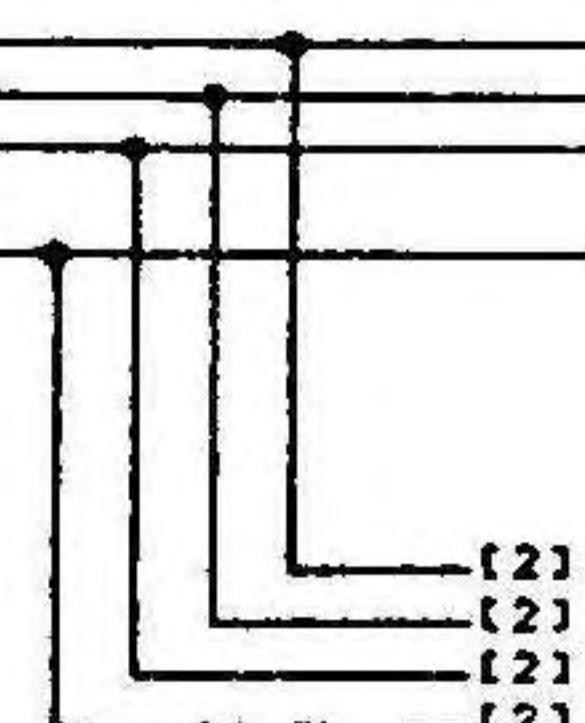
RT-FDBK-CLK	P1 0	P7 85
RT-FDBK-RST	P1 1	P7 4E
LF-FDBK-CLK	P1 2	P7 86
LF-FDBK-RST	P1 3	P7 44
RT-DRIVE	P1 4	P7 87
RT-DIR	P1 5	P7 48
LF-DRIVE	P1 6	P7 88
LF-DIR	P1 7	P7 46
		P7 26
		P7 23
		P7 82
		P7 40

SERIAL BUS IN

P6 01
P6 03
P6 05
P6 07
P6 09
P6 02
P6 04
P6 06
P6 08
P6 10

PRIORITY OK IN

[2]



BUS SLAVE RCU
BUS SLAVE XMIT
BUS BUSY

[2] — PRIORITY OK OUT

SYSTEM RST

SERIAL BUS OUT

P6 01
P6 03
P6 05
P6 07
P6 09
P6 02
P6 04
P6 06
P6 08
P6 10

ANDROBOT, INC.

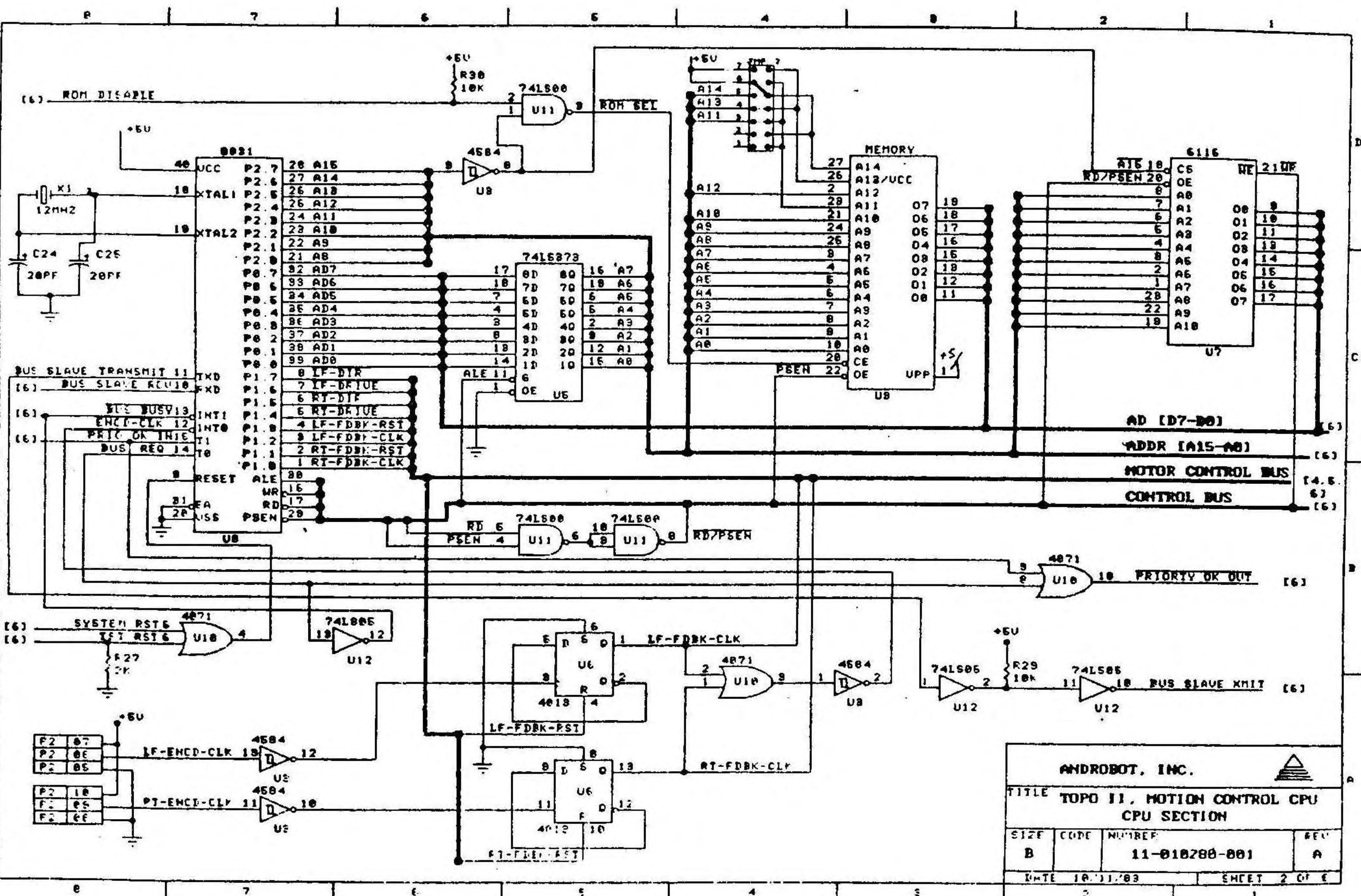


TITLE TOPO II, MOTION CONTROL CPU
CONNECTOR PINOUT

SIZE	CAGE	NUMBER	REF
B		11-010200-001	A

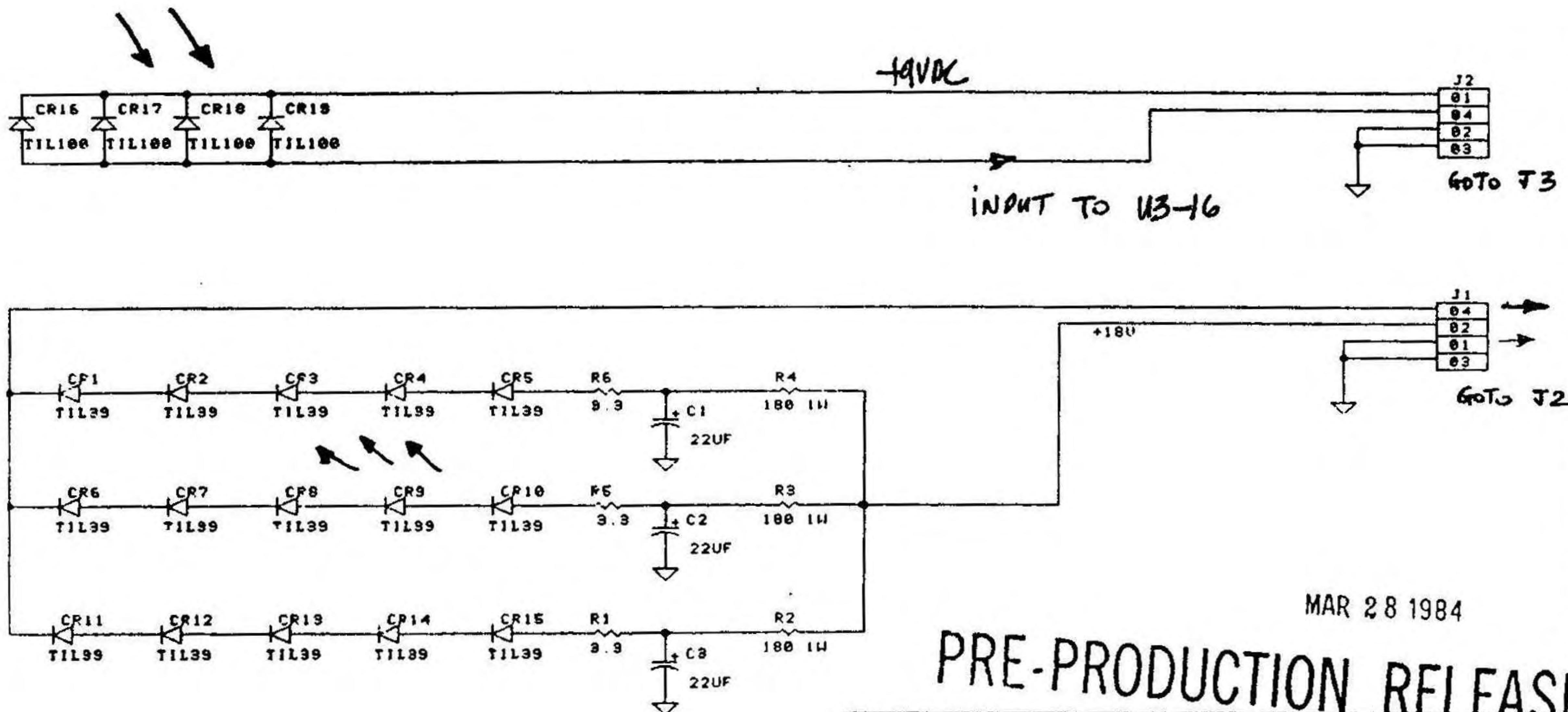
DATE 10/11/88

SHEET 1 OF 1



6 ? 6 5 4 3 2 1

REVISIONS			
LTR	DESCRIPTION	DATE	APUD
A	PRE-PROD REL ECO 80015	10/21/83	L H
B	REVISIE ECO 80050	01/17/84	
C	ECO 80081	1/30/84	9C

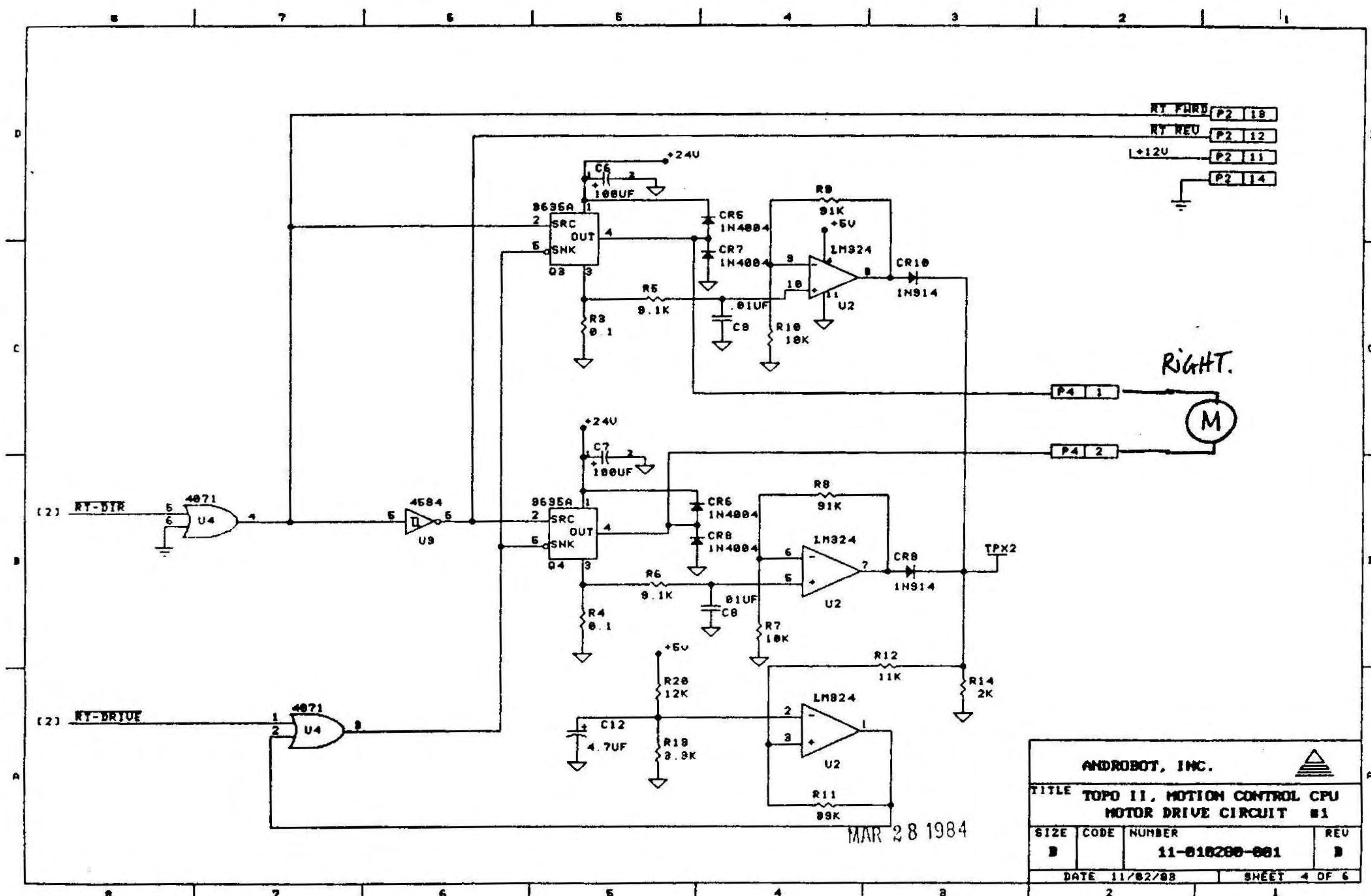


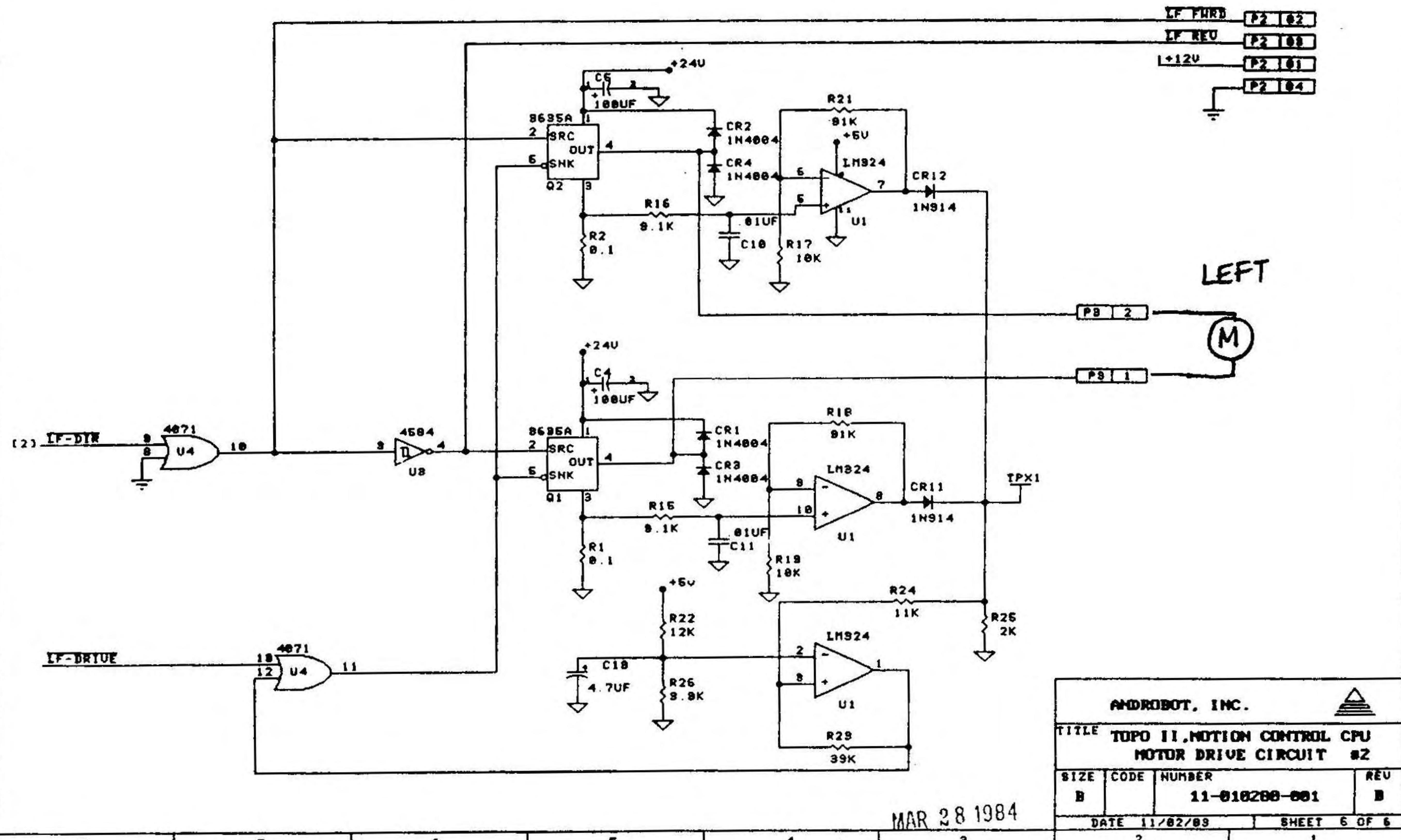
MAR 28 1984

PRE-PRODUCTION RELEASE

DATE	01/30/84	ANDROBOT, INC.	
DWII	J YOUNG		
APUD			
APUD			
ENGFB			
CHFR			
TITLE		MOBILE TRANSEIVER SENSOR	
SIZE	CODE	NUMBER	REV
B		11-010369-001	C

SHEET 1 OF 1





PREF-PRODUCTION RELEASE

8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

ADDR [A15-A0]

[2]	A15	P2.7	P7 39
	A14	P2.6	P7 18
D	A13	P2.5	P7 84
	A12	P2.4	P7 17
	A11	P2.3	P7 96
	A10	P2.2	P7 16
C	A9	P2.1	P7 86
	A8	P2.0	P7 15
	A7		P7 37
	A6		P7 14
	A5		P7 98
	A4		P7 19
	A3		P7 39
	A2		P7 12
	A1		P7 40
	A0		P7 11
	AD7	P8.7	P7 28
	AD6	P8.6	P7 22
	AD5	P8.5	P7 86
	AD4	P8.4	P7 21
	AD3	P8.3	P7 91
	AD2	P8.2	P7 20
	AD1	P8.1	P7 32
	AD0	P8.0	P7 18
	ALE		P7 8
	WR		P7 42
	RD		P7 10
	PSEN		P7 41

MOTOR CONTROL BUS

[2]	RT-FDBK-CLK	P1.0	P7 65
	RT-FDBK-RST	P1.1	P7 46
	LF-FDBK-CLK	P1.2	P7 86
	LF-FDBK-RST	P1.3	P7 44
	RT-DRIVE	P1.4	P7 67
	RT-DIR	P1.5	P7 43
	LF-DRIVE	P1.6	P7 68
	LF-DIR	P1.7	P7 46
			P7 26
			P7 28
			P7 83
			P7 48

ROM DISABLE (TEST USE ONLY)
TEST REBET (TEST USE ONLY)
+5V

SERIAL BUS IN

P6 01	
P6 03	
P6 05	
P6 07	PRIORITY OK IN
P6 09	[2]
P6 02	
P6 04	
P6 06	
P6 08	
P6 10	

BUS SLAVE RCU
BUS SLAVE XMIT
BUS BUSY
[2] PRIORITY OK OUT
SYSTEM RST

SERIAL BUS OUT

P6 01	
P6 03	
P6 05	
P6 07	
P6 09	
P6 02	
P6 04	
P6 06	
P6 08	
P6 10	

MAR 28 1984

ANDROBOT, INC.			▲
TITLE TOPO II, MOTION CONTROL CPU CONNECTOR PINOUT			
SIZE	CODE	NUMBER	REV
B		11-010280-001	B
DATE 11/02/88		SHEET 6 OF 6	

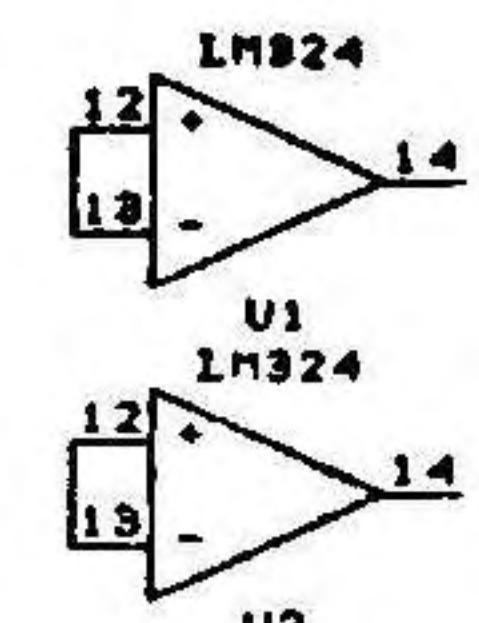
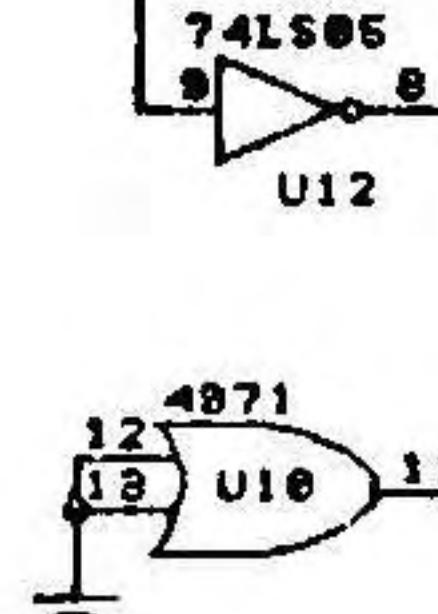
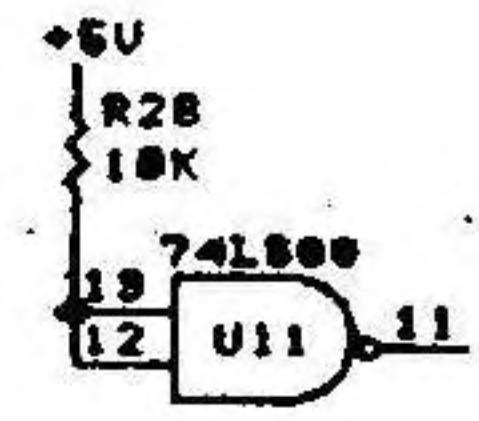
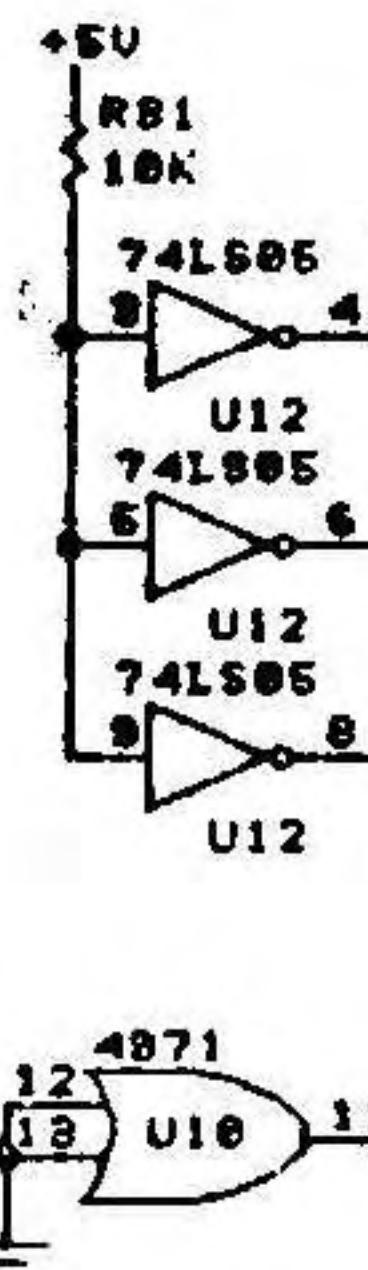
PRE-PRODUCTION RELEASE

REVISIONS

REV	DESCRIPTION	DATE	APP'D
A	PROD-PROD REL, ELC 00019	10/21/83	AG

IC TABLE

IC TYPE	REF DESIGNATOR	QTY	+5V	SND	-5V	+12	+24
8021	U6	1	40	20			
2764	UR	1	20	14			
6116	U7	1	24	12			
4019	U6	1	14	67			
4071	U4 U10	2	14	67			
4684	U9	1	14	67			
741500	U11	1	14	67			
741505	U12	1	14	67			
7415373	UE	1	20	10			
LM824	U1 U2	2	84	11			
LM7805	UR1	1	68	62		81	
SG3695	01.02.03.04	4				81	

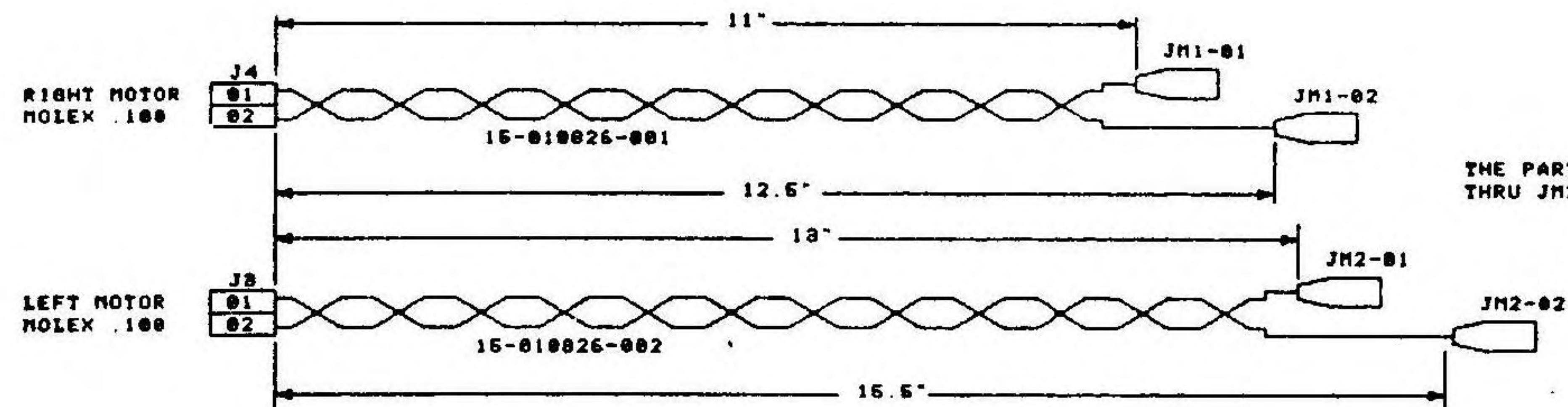


NOTE

ALL BYPASS CAPACITORS ARE 1UF IN VALUE.
DESIGNATORS FOR CAPS ARE C14 THRU C23.

DATE	10/21/83	ANDROBOT, INC.		
DESIGN				
W/C/P/L	64			
APL				
L/W/H				
REV C/P/T				
SIZE	CODE	NUMBER	FILE	
B		11-010200-001	A	
D-TF		10 11/83	SHEET 1 OF 6	

8	7	6	5	4	3	2	1	REVISIONS				
								ZONE	TIR	DESCRIPTION	DATE	APU



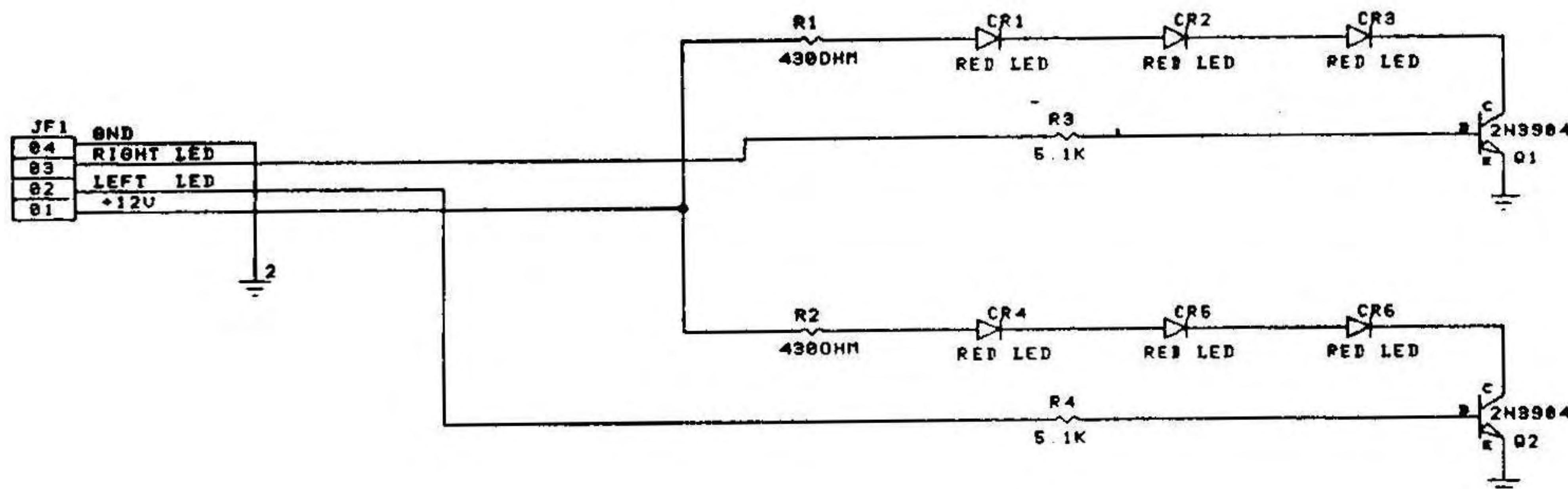
MATING NOTE

ITEM	DESIGNATOR	WIRE COLOR	LENGTH (IN.)	TERMINATION PT.	GUAGE
1	J4-01	RED	11.0	JM1-01	18
2	J4-02	BLACK	12.5	JM1-02	18
3	J3-01	RED	13.0	JM2-01	18
4	J3-02	BLACK	15.5	JM2-02	18

DATE	10/18/89	ANDROBOT, INC. 	TITLE TOPO II MOTOR HARNESS ASSEMBLY	SIZE CODE NUMBER 15-010826-001,002 A	REL
DUN	T SAKAMOTO				
AFUD					
APUD					
ENGRG					
CHKR					

SHEET 1 OF

REVOLUTIONS				
ZONE	LTR	DESCRIPTION	DATE	APUD
A	PRE-PROD	EL	1/21/84	2-7-10



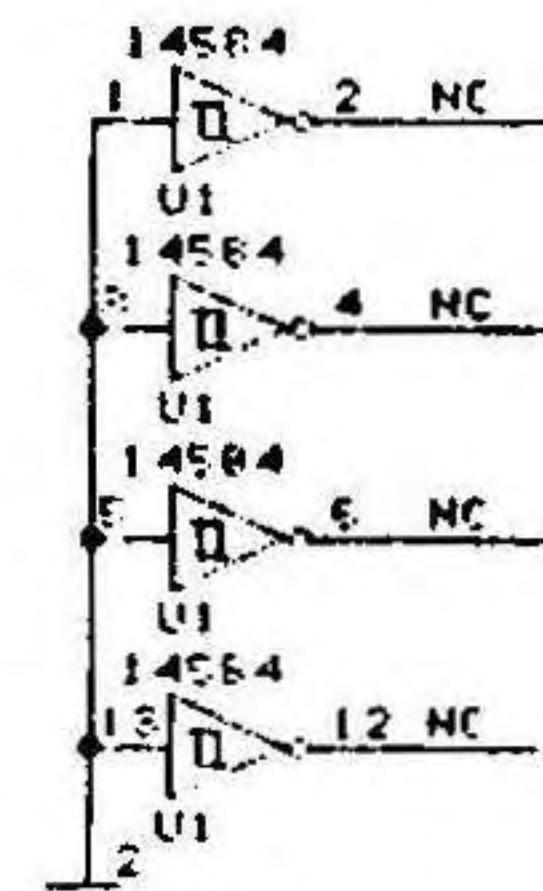
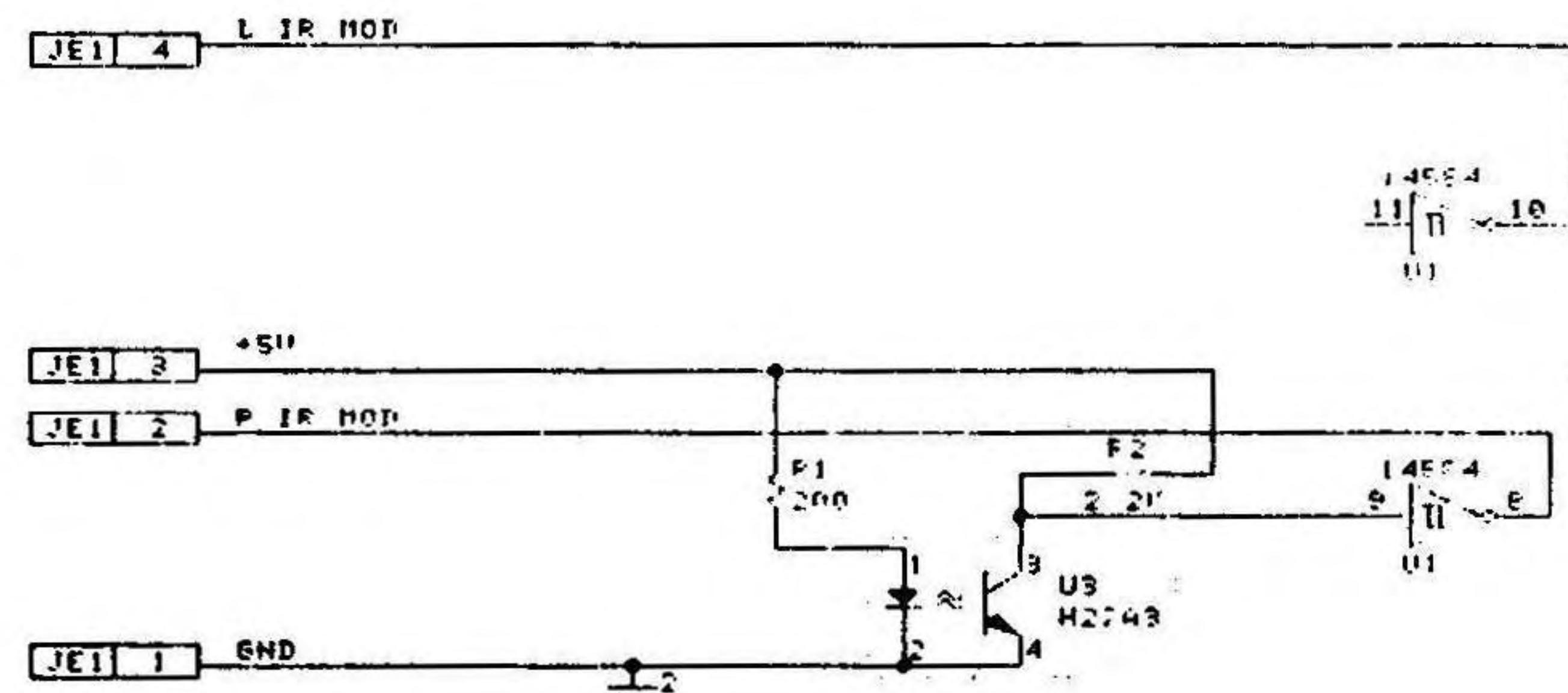
PRE-PRODUCTION RELEASE

MAR 28 1984

DATE	10/26/83	ANDROBOT, INC.
DNN	T. SAKAMOTO	
APUD		
APUD		
ENGRG		
CHKR		
TITLE		TOPO 11
		FOOTLIGHT SCHEMATIC
SIZE	CODE	NUMBER
B		11-010249-001
		A
SHEET 1 OF 1		

REVISIONS		
REV	DESCRIPTION	DATE
A	PRE-PROD REL ECU REV A	APR 1983
A1	ECC CTR 93	

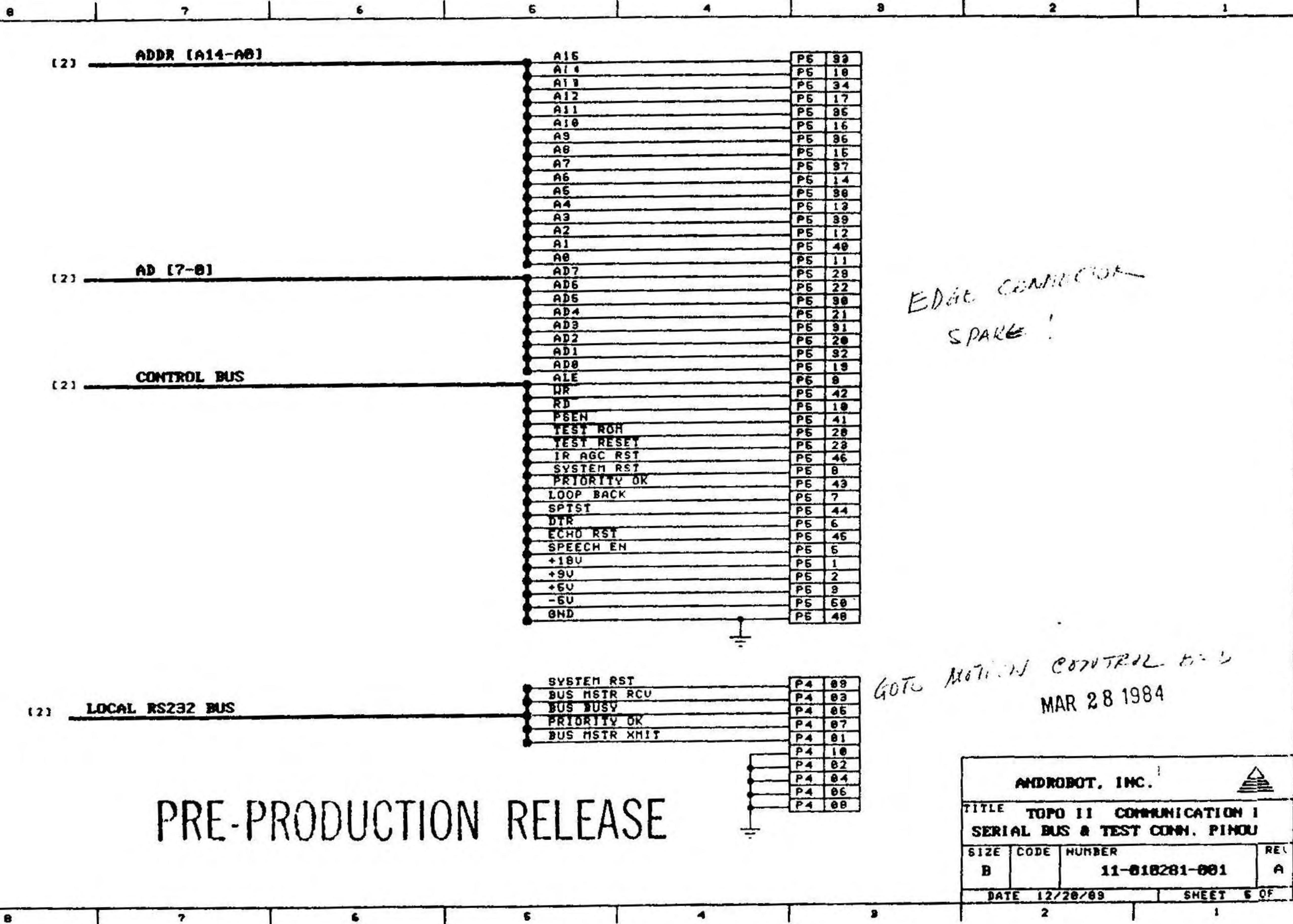
REVISIONS		DATE	REF	DESCRIPTION	DATE	REF
A	IR MOD	10/20/83	14584	ECU REV A	10/20/83	14584



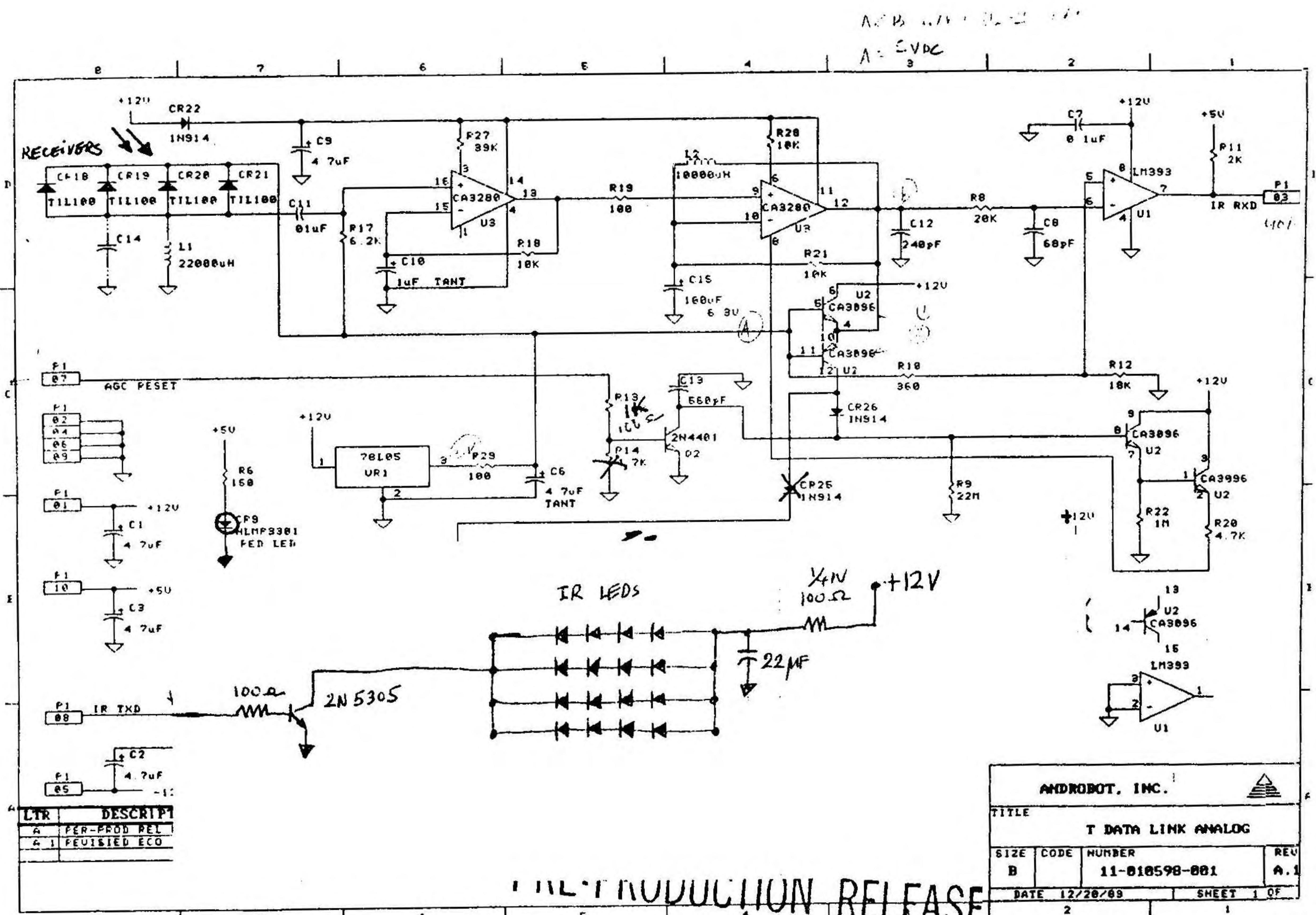
PRE-PRODUCTION RELEASE

MAR 28 1984

DATE	10/20/83	ANDROBOT. INC.
DESIGN	1 YOUNG	
REV'D		
REV'D		
ENGINEER	F.Y. ✓	
CHEK		
TITLE		TOPO II
		MOTOR ENCODER BRD.
SIZE	CODE	NUMBER
B		11-010257-001
		REL
		A
SHEET		1 OF

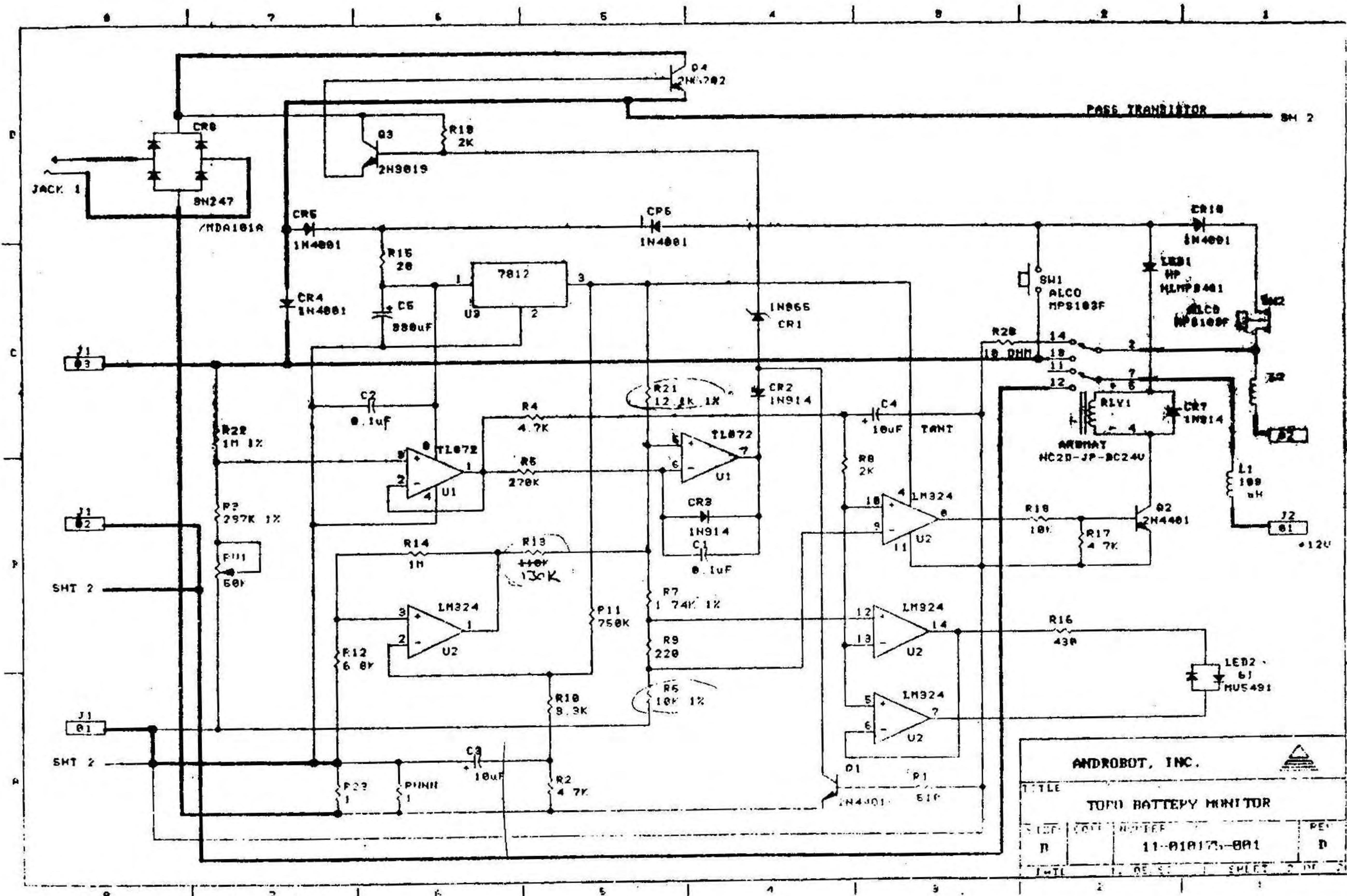


ANDROBOT, INC.		
TITLE TOPO II COMMUNICATION I		
SERIAL BUS & TEST CONN. PINOUT		
SIZE	CODE	NUMBER
B		11-818281-001
DATE 12/20/89		SHEET 5 OF



⑧ CUT OPEN

24/8
3



error code	cause
-----	-----
1100	Can't write and read one byte of value 0 at ram address 0.
1101	Can't write and read a value 1 in bit 0 addrs 0
1102	Can't write and read a value 1 in bit 1 addrs 0
1103	Can't write and read a value 1 in bit 2 addrs 0
1104	Can't write and read a value 1 in bit 3 addrs 0
1105	Can't write and read a value 1 in bit 4 addrs 0
1106	Can't write and read a value 1 in bit 5 addrs 0
1107	Can't write and read a value 1 in bit 6 addrs 0
1108	Can't write and read a value 1 in bit 7 addrs 0
1110	Can't write and read a pattern into/out of ram at addresses 0-FFH.
1111	Can't read the previous pattern back from ram addresses 0-FFH.
2001	All odd bytes are of value 0.
2002	All even bytes are of value 0.
3101	The pull-ups on ports B & C look like grounds.
3102	The ports A and C report something other than value 0 when port B is output with value 0.
3103	The ports A and C report something other than the test pattern output on port B.
3104	The ports A and B report something other than value 0 when port C is output with value 0.
3105	The ports A and B report something other than the test pattern output on port C.

COMMUNICATION BOARD TEST
NUMBER DEFINITIONS

23 MAY 1984

error code	cause
-----	-----
1100	Can't write and read one byte of value 0 at ram address 0.
1101	Can't write and read a value 1 in bit 0 addrs 0
1102	Can't write and read a value 1 in bit 1 addrs 0
1103	Can't write and read a value 1 in bit 2 addrs 0
1104	Can't write and read a value 1 in bit 3 addrs 0
1105	Can't write and read a value 1 in bit 4 addrs 0
1106	Can't write and read a value 1 in bit 5 addrs 0
1107	Can't write and read a value 1 in bit 6 addrs 0
1108	Can't write and read a value 1 in bit 7 addrs 0
1110	Can't write and read a pattern into/out of ram at addresses 0-FFH.
1111	Can't read the previous pattern back from ram addresses 0-FFH.
2001	All odd bytes are of value 0.
2002	All even bytes are of value 0.
3101	The pull-ups on ports B & C look like grounds.
3102	The ports A and C report something other than value 0 when port B is output with value 0.
3103	The ports A and C report something other than the test pattern output on port B.
3104	The ports A and B report something other than value 0 when port C is output with value 0.
3105	The ports A and B report something other than the test pattern output on port C.

COMMUNICATION BOARD TEST
Number DEFINITIONS

23 MAY 1984

ANDROBOT TOPO I/O

Solder Side

(Corrected)

Component Side

